Accellera ALC – C++ Initiative Working Group meeting 8/24/2000

Present at Intel:

> Prof. Gajski (chair) [UCI]
> Kevin Kranen [Synopsys/Open System C]
> David Springer [CynApps]
> Asa Ben-Tzur [Intel]
> Frederic Doucet [UCI/Intel] frederic.j.doucet@intel.com
> Martin Baynes [C Level Design] (notes)

Absent:

> Brian Bailey (co-chair) – understood at time of scheduling meeting.

The meeting convened at 9:45am, following the ALC conference call. The start was delayed to accommodate Vassilios' scheduling conflict.

Agenda:
1. Prog. Gajski reviewed RT Semantics document (Abstracted), (published on Web for today's meeting) – intent to define semantics for levels 1-5 in his last presentation. Illustrations included VHDL and Spec C.
2. Committee refined the rules for RTL design within Prof Gajski's framework – these are below.
3. Next steps:
   a. Martin to distribute rules/minutes (Monday Aug 28) & work with Brian to publish minutes summary only for now.
   b. Updates to rough notes of rules to Martin in time to incorporate before September 7 presentation, if possible, otherwise Martin will prepare a cleaned up version & distribute before the meeting and present at the meeting.
   c. All to supply Asa with examples circuits (Friday August 25). Minimum of two communicating FSMDs.
   d. Asa to choose one example for all to complete (Monday Aug 28)
   e. All (who are willing) to complete example and present next full meeting (Sept 7)
   f. All (who are willing) to schedule specifying each case & commit (Sept 7)
1. Next meetings:
   a. Sept 7, full meeting at Mentor.
   b. Last week in September/1st week of October, detail schedule & host to be confirmed September 7.

The meeting closed before 3pm, having accomplished all the planned business.

Respectfully submitted,

Martin Baynes