ID .	Task Nama		Duration	Finiah	Qtr 4, 1999	Qtr 1, 2000	Qtr 2, 2000	Qtr 3, 2000	Qtr 4, 2000	Qt
	Task Name 1 DC-WG		Duration 150d	Finish 5/23	Oct Nov Dec	; Jan Feb Mai	r Apr May Jun	Jul Aug Sep	Oct Nov Dec	Jan
2	1.1 Proposals for unresolved areas		20d	11/23			•			
3	1.1.1 Are tags required?		2w	11/9						
1	1.1.2 Specifics for modes		2w	11/9						
5	1.1.3 Specifics for precedence	ce rules	4w	11/23						
	1.1.4 Partial resetting of cons	straints	4w	11/23						
	1.2 Review and revise the spec		150d	5/23						
	1.2.1 Basic language features		2w	11/9	►		•			
	1.2.2 Universal commands and features		2w	11/23						
)	1.2.3 Scoping commands and operating conditions		2w	12/7						
	1.2.4 Synchronous theory and common conventions		1w	12/14	Т.					
2	1.2.5 Relationship with OLA, clock commands part 1		Зw	1/4						
3	1.2.6 Clock commands part 2		2w	1/18		 ▶				
•	1.2.7 Timing boundary commands		2w	2/1						
;	1.2.8 Multi-cycle commands		2w	2/15						
;	1.2.9 Disables, false paths, b	orrow limits	2w	2/29						
,	1.2.10 Parasitic boundary conditions theory		2w	3/14						
3	1.2.11 Wire load models, external loads, detailed parasitics		2w	3/28			Ъ			
)	1.2.12 Driver specifications		2w	4/11			- L			
)	1.2.13 Compliance		2w	4/25			Ē.			
I	1.2.14 Glossary		2w	5/9			_ _			
2	1.2.15 BNF, Index		2w	5/23						
roject: Q4 1999 Plan Detailed Schedule Task		Milestone	•		Rolled Up Task		Rolled Up Prog	iress		
sion:	0.1	Progress	Summary			Rolled Up Milestor	ne $<>$			