































17	[Begin Header]	
	[ICM Ver]	1.0
	[File Name]	iconm_hdi_202.icm
	[File Rev]	1.0
	[Date]	May 29, 2003
	[Source]	Results from field simulation
	[Notes]	This is a test model only.
	[Disclaimer]	This information is for modeling purposes only, and is not guaranteed.
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	[Support]	http://www.VendorNameIbisModels.com
	[Redistribution]	Yes
	[Redistribution Text] This file is freely redistributable.
	[End Header]	

	ICM Example (2)						
	[Begin ICM Family] H [Manufacturer] X [ICM Family Description	YZ Incorporated	onnect				
	High Density square pin [ICM Model List]						
	Name	Mating Min	_Slew_T	ime Image			
	My ModelExample3	Mated	100ps	HDI_202_Mated.jpg			
	HDI_202_UnMatedA	Unmated_side_A	100ps	HDI_202_UnMatedA.jpg			
	HDI_202_SMT_to_Cable	Mated	25ps	HDI_TEST_202_Mated.jpg			
	HDI_202_SMT_to_ThruHole	e Mated	25ps	HDI_202_Mated.jpg			
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	ICM Example (3)					
<u>V</u>]	<pre>[Begin ICM Model] MyModelExample3 Has a stub fork! ICM_Model_Type MLM [Begin ICM Model Description] High Density 0.1 center square pin with PCB effects</pre>					
	[Tree Path Description]					
	Model_PinMap Baseboard_side					
	Section Mult=1 SectionA					
	Fork					
	Section Mult=1 StubSection1					
	End_fork					
	Section Mult=1 SectionB					
	Model_PinMap Output_side					
	[End ICM Model]					
	SectionA SectionB					
	Baseboard_side > Output_side					
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	IC	CM Example	(4)			•	
	[ICM Pin Map] Baseboard_side						
	Pin_order = Row_ordered						
	Num_c	of_columns = 4					
	Num_o	f_rows = 2					
	Pin_1	ist	A1	A2	A3	A4	
	Pin	Name					
	A1	PERR#	$\left \bigcirc \right $	\bigcirc	\bigcirc		
	A2	STOP	\bigcirc	\bigcirc	\bigcirc	\bigcirc	
	A3	AD12	\cup	\bigcirc	\bigcirc		
	A4	AD15	B1	B2	B3	B4	
	в1	STOP					
	в2	GND					
	в3	PAR					
	в4	GND					
	[ICM	[Pin Map] Output_side om	itted :	Eor cl	arity		
	[End	ICM Family]					
നപ്പ	05/29/03 "Other brands ar	nd names are the property of their respective owners	Page 2	D		Desktop Platforms	
						GROUP	

ICM Example (5) ۰ |-----[Begin ICM Section] SectionA [Derivation Method] Lumped [Inductance Matrix] Full_matrix [Row] 1 3.04859e-07 4.73185e-08 1.3428e-08 6.12191e-09 1.74022e-07 7.35469e-08 2.73201e-08 1.33807e-08 [Row] 2 1.3428e-08 7.35469e-08 3.04859e-07 4.73185e-08 1.74022e-07 7.35469e-08 2.73201e-08 1. 1. 1. intel. 05/29/03 *Other brands and names are the property of their respective owners Page 21 Desktop Platforms GROUP

