<CHECKLIST HEADER>
    Filename
    Revision #
    Ibis Version
    Description
    Notes
    Summary of quality per model   Note: (Using "|IQ" comment string)
    Summary of quality of every item that can have quality

<BEGIN CHECKLIST>

General File Characteristics:

	Complete	Correct	Optional	Notes
Reference to Vendor Component Number				
Pin Mapping				
Passes ibischk?				Version
Number of errors				Explanation
Number or warnings				Explanation
Origin of model				Lab/Spice/Conditions/Spice decks
SPICE (tool name and Version)				
SPICE model - <NAME><REV>				
Corner process(es)				
Package model - <NAME><REV>				
Portability				
Tested on XX tools.				
Translated to XX tools.				

Component Section:

	Complete	Correct	Optional	Notes
Every pin represented				
Pin Mapping				
Any pin mapping that cannot be currently modeled				
Logical/Physical/Model mapping				
External Package model - <NAME>				
Generic pkg values		Reasonable?		Tracibility
Specifc pin pkg values				
Diff Pin				
Model Selector				Correct defaults
Correct default model in Model Selector				

Model section: (For each model)

	Complete	Correct	Optional	Notes
Model type				
Proper Curves Include				
Proper Thresholds				
Proper keywords included for model type.				Still need to define min/max keywords.
Temperature Range				
Corner models included				
Voltage Range				
Typ/min/max included everywhere applicable				
Proper min/typ/max ordering				
C_Comp				
What is source?				Sim/measurement/etc.
How value arrived at?				For driver, receiver, combination?
Standard Load Info:				(Document if IBIS doesn't support needed Std. Load)
Vref				Should match datasheet
Cref				Should match datasheet
Rref				Should match datasheet
ALL curves generated under identical corner conditions?				
Proper compares between Ramp Data and Vt curves				Document reason.

IV Curves:

	Complete	Correct	Optional	Notes
Pullup waveform - Visual Check				
Pullup Reference				
Pulldown waveform - Visual Check				
Pulldown Reference				
Excessive Currents				
Clamp curves subtracted				
Monotonic				
Bounded by Data Sheet?				

Clamp Curves:

	Complete	Correct	Optional	Notes
Monotonic				
Non-overlapping				
Excessive Clamp Currents				
GND clamp - Visual Check				
Excessive Currents				
End points correct				
Turn-on points				
Orientation				
Zero crossing point				
GND Clamp Ref.				
Summation of curves monotonic				
POWER Clamp - Visual Check				
Excessive Currents				
End points correct				
Turn-on points				
Orientation				
Zero crossing point				
POWER Clamp Ref.				
Summation of curves monotonic				

VT Curves:

	Complete	Correct	Optional	Notes
Monotonic				
Rising waveform - Visual Check				Turn on/off times.
Falling waveform - Visual Check				
Reference Voltage				
Fixture				Discuss
Consistent w/ IV Curves				
Leading/trailing delays removed				For portability
Multiple VT curves?				Want to avoid
Enough points in the transistion region?				
Proper lead-in Time				
Timestep appropriate				
Achieve DC state				

Ramp data:

	Complete	Correct	Optional	Notes
				Correlate to VT curves
Is dV 20%-80%				
typ/min/max order				
No negative values				
No zero values				

Waveform processing requirements:

	Complete	Correct	Optional	SOURCE of value:
Vih				
Vil				
Vmeas (Model section)				
Vinh+				
Vinh-				
Vinl+				
Vinl-				
Pulse_high				
Pulse_low				
Pulse_time				
S_overshoot_high				
S_overshoot_low				
D_overshoot_high				
D_overshoot_low				
D_overshoot_time				
Vmeas (Model Spec section)				

Possible Errors that should be checked for:
- typ/min/max swizzles
- Hole between gnd/power clamps and IV Curves
- Ramp > 100% swing
- Ramp # waveform (but same Rload)
- V-fixture # of waveforms (ie RISING/FALLING to pullup/pulldown)
- GTL-model with different Vref,Cref,Rref,Vmeas
- Differential models only with ramp
- Tristate-current not 0ma
- Excessive currents
- To few points in waveform
- C-comp same value for in and io

Spice Correlation
- Accuracy level achieved per IBIS Accuracy Spec. (Figure of merit)
- Buffer strength (output impedance and DC operating point)
- Rising/falling edges (duration and wave shape)
- Reflected wave behavior (primarily tests value of C_COMP/Clamps)

Lab Correlation
- Accuracy level achieved per IBIS Accuracy Spec.