OK Technical Committee meeting, October 22, 2003

Attendees

Paul Koch, Cadence	Simon Klaver, Sagantec
Wolfgang Roethig, NEC	Pallab Chatterjee, SiliconMap
Mahmoud Shahram, Synopsys	Henry Chi, Mentor
Michael Alston, Artisan	Mark Vaughan, AWR
George Swan, Barcelona	Brian Bart, Silvaco
David Lan, TSMC	Andy Brotman, Conexant

Overview

The meeting was spent in preparing for a request for submissions of design kit documentation and technology.

Planning for the Request For Submissions

There was good, open discussion at this meeting that created an ability to simplify our approach and get traction quickly.

We agreed to target three to five categories of areas to request initial submissions. The three areas we selected to work in first are the following

1   Standard symbol set for design capture (front end team)

2   Physical design rule formats and naming conventions, layer representation standards (physical team)

3   Standard datasheet formats for commonly used devices (datasheet team)

It was felt that these were large, important areas that touched both the EDA vendor community and the foundry community, the two communities that are the major suppliers of design kits to other companies.

Since not everyone is an expert in all of these areas, we will need to form ad hoc investigation teams to participate in the evaluation of each of the submissions in these technology areas. This is therefore also a call to the TC participants to let me know to which area you would like to contribute. People may participate in more than one area, and we will establish pages on the website for each team to keep communications open to the rest of OK and Accellera.

1   Front End Team

2   Physical Team

3   Datasheet Team

Please reply with your interest in serving on one or more of these teams.

As always, if I have left something out or if you have any other corrections, please reply with your comments and I will amend this document.

Nick English