Advanced WAVES

RASSP E&F Module 62

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Abstract:

The *Advanced WAVES* module discusses the advanced ideas of a VHDL testbench, and its use in the design process. As part of the testbench discussion, the standard for VHDL Waveform and Vector Exchange (WAVES) - IEEE Std 1029.1 1996 - is used as a methodology for describing the input stimulus to be applied to the Unit Under Test (UUT) and the expected outputs which are compared to the actual outputs in the testbench.

Specific Objectives:

Provide information on:
1. Match Functionality
2. Handshake Functionality
3. Multiple Waveform Generators
4. Multiple Timing Sets And Bidirectional Test Pins

Prerequisites:

Prerequisite Modules:
VHDL modules 10-13, 43 & 61.

Prerequisite Knowledge:
Familiarity with testing in VHDL and Basic WAVES.