# **SystemVerilog Interfacing to Foreign Languages**

#### Overview

Since the foreign language most often used is C and C linkage is well defined, it is assumed that all foreign code will use the same interface as C for direct linking of code. There is an assumption in this document that 2-state (C compatible) data types are passable by reference (pointer) and that C function prototypes are passable in SV (since it desirable to minimize re-writing them).

### **Calling C From SV**

To call a C routine from SV requires an external declaration in the SV source. Direct binding through code linkage requires that has a valid C name, since names in Verilog may not be valid an optional C name can be added to the external declaration. External routines can also be attributed "pure" implying that there are no side-effects, needing context, or "static" which implies dynamic binding<sup>1</sup>. The complete syntax is:

To declare a standard C function like "write" which is context free one could use the following code in SV:

```
extern int write(int, const char *buf, int);
```

If the using write is not going to feedback into the simulation and it is to replace a differently name SV routine one could use:

```
extern pure int sv_write(int, const char *buf, int) "write";
```

Functions requiring context are called with two extra arguments: a PLI handle for the calling instance (which may be \$root) and a pointer to some permanently allocated memory which the callee can use for maintaining state. The structure of the permanently allocated space is described in C as:

```
typedef struct {
union {
```

<sup>1.</sup> There is no direct C entrypoint so no symbol table entry.

```
void
                       *ptr;
               int
                      data[2];
               double dbl;
          } user_context; /* user-context, initialy zero, 64 bits (aligned) */
                                                     /* = 0, to allow for version changes */
           short
                          context version;
           short
                          call num;
                                                     /* call on line */
                                                     /* source line number */
                          line_number;
           int
                          *file_name;
                                                     /* may be null string "" but not null*/
           const char
} svcContext;
```

This can be used by routines for reporting errors, coverage testing etc. and the callee can write its own values into the user\_context union e.g. a pointer to another array of memory. The context\_version field is zero for this struct, other versions which overlay it will use other values. For example, the following declaration in SV:

If the static version of the external declaration is used, then the user code must register a locator routine before the simulation initializes so that the simulator can link up routines that are left undefined by elaboration. The registration routine is svcRegisterLocater:

```
... /* argument types */);
```

int svcRegisterLocator(svcLocater,void \*locator\_context,char \*module\_spec, int capabilities)

This method allows modules to bind to C routines which are static or are overloaded in C++, the following code demonstrates how to set up binding for a C++ function whose name is not predictable<sup>1</sup>: The capabilities argument of the locator is reserved for future enhancements; like changing how the arguments are specified, in this version capabilities should be zero and argument types are given as C strings.

```
extern "C" {
#include "sv2c.h" // svcRegisterLocator etc.
static pointer myBinder(void *,char *,char *,char *,void **,...);
class root_cpp {
   root cpp() {svcRegisterLocator(myBinder,this,"$root",0); ]
}
static root_cpp RootCpp; // calls svcRegisterLocator when constructed
static int cpp_routine(handle inst,svcContext *p_context,int data) {
   // a routine called from SV: extern context int cpp_routine(int);
}
static svcExtFunc myBinder( void *context,char *modname,char *inst name,char *rtn name,
                              svcContext *p_context,...)
{
   svcExtFunc rtn\_addr = 0;
   if (0 == strcmp(rtn_name,"cpp_routine")) rtn_addr = cpp_routine;
   if (rtn_addr) p_context->user_context.ptr = context; // set context for future calls.
   return rtn_addr;
```

The binder function is called once for each context (line and position) in which SV calls the routine so that it can intialize all contexts. The extra arguments to the binder function are the types of the arguments to the routine call in SV, and may vary from context to context; the list values are C strings and is terminated with a null pointer. The SV simulator will report a fatal error if no match is found or

<sup>1.</sup> Due to name mangling by the C++ compiler.

multiple matches are found with the same priority. Priority is determined by the matching of the *module\_spec* argument of the locator, an exact match takes priority over a wild-card which take priority over no module specification (null), binder functions are not called if there *module\_spec* does not match the instance.

### **Calling SV from C**

To access an SV task or function from C it needs an "export" declaration. Export declarations can be placed in modules for exporting module specific tasks and functions or in \$root for global tasks and functions, and module specific tasks and functions can also be exported from root using the special syntax "<module spec>::" in front of the task or function name:

```
export_decl ::= export attribute (, attribute) * ?

[module spec::]fname "C name" ?;

attribute ::= static

module spec ::= (modulename|interfacename) (::(modulename|interfacename))* ?

[modulename]interfacename) (::(modulename|interfacename)) * ?

[modulename]interfacename) (::(modulename|interfacename)) * ?

[modulename]interfacename) (::(modulename|interfacename)) * ?

[modulename]interfacename]interfacename)
```

As with external declarations the SV name may not be a valid name so a valid C name can be provided. Similarly declaring the export as "static" implies only dynamic binding is used. Routines and tasks exported from \$root are context free, routines and tasks exported from modules need to be called with a reference to the specific instance the call applies to. Standard PLI functions can be used to find a particular instance and the instance handle is passed by the caller as the first argument.

Non-static exports are handled by the the linker, dynamic linking of context dependent SVroutines is performed after elaboration. The foreign code calls the SV simulator function svcLocateTF to obtain a pointer to the appropriate entry point:

```
/* Template for an exported context task or function */
typedef int (*svcContextTF)(handle pli_inst,...);
/* Locater function */
svcContextTF svcLocateTF(handle pli_inst,int capabilities,const char *tf_name,...);
```

The extra arguments to svcLocateTF are the types of the arguments the caller is expecting to use, and as with external binders the capabilities argument is reserved for future modification of that interface. The following example shows a simple dynamic binding.

```
module cboundary;
task munge(input d);
int d;
```

<sup>1.</sup> top.foo would refer to a module foo declared inside module top

```
endtask
...
endmodule
```

export static cboundary::munge; // create a static C entry point for task foo in module cboundary

The corresponding C code could be:

The final argument to svcLocateTF can be "..." instead of 0 if the eported function is called with different argument lists, the return value from svcLocateTF will be null if the simulator cannot support or cannot find a match ing task or function.

## Data Passing between SV and C

The sections above describe how exported and external routines are linked. The passing of data on the calls is either direct (in registers or on the stack) or indirect through an abstract interface...

**TBC**