

Cliff votes "no" to Mantis 687 but that vote will change to a "yes" if the following corrections and changes are accepted.

WAS: The ‘begin_keywords and ‘end_keywords directives can only be specified outside of a **module or primitive**. The ‘begin_keywords directive affects all **modules or primitives** that follow the directive, even across source code file boundaries, until the matching ‘end_keywords directive is encountered.

PROPOSED: The ‘begin_keywords and ‘end_keywords directives can only be specified outside of a **design element (module, macromodule, primitive or configuration)**. The ‘begin_keywords directive affects all **design elements** that follow the directive, even across source code file boundaries, until the matching ‘end_keywords directive is encountered.

PROPOSED: Global changes in both P1364-2005 and P1800-2005 documents

P1365-1995 to 1364-1995

P1365-2001 to 1364-2001

P1365-2005 to 1364-2005

P1800-2005 to 1800-2005

Even the unapproved documents should drop the "P." It is used inconsistently throughout the sections (examples do not have leading "P"'s but most, not all, textual references do have leading "P"'s).

Table 19.x — IEEE P1364-2005 reserved keywords

Changed keywords per mantis 680

WAS:

cell

config

endconfig

PROPOSED:

cellname

configuration

endconfiguration

26.4 ‘begin_keywords, ‘end_keywords

(typo - P1364 is NOT SystemVerilog!)

WAS: **SystemVerilog** extends the ‘begin_keywords and ‘end_keywords defined in the **P1364-2005** standard by adding an "1800-2005" *version_specifier*.

PROPOSED: **SystemVerilog** extends the ‘begin_keywords and ‘end_keywords defined in the **1800-2005** standard by adding an "1800-2005" *version_specifier*.

Table 26.x — IEEE P1800-2005 reserved keywords

Changed keywords per mantis 680

WAS:

cell
config
endconfig

PROPOSED:

cellname
configuration
endconfiguration

The ballot copy of the P1800-2005 had the following paragraph, which has apparently been removed.

The 'begin_keywords and 'end_keywords directives can only be specified outside of a **module**, **primitive**, **interface**, **program** or **package**. The 'begin_keywords directive affects all modules, primitives, interfaces, programs or packages that follow the directive, even across source code file boundaries, until the matching 'end_keywords directive is encountered.

*The P1364-2005 ballot draft specifies the legal design elements. Are we not going to list legal SystemVerilog design elements (including **configurations**) in the P1800-2005 standard?*