



# SystemVerilog 3.1a SV-CC Update

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# Status

- **Extensions**

- **All donations have been reviewed and have been accepted.**
- Extension for non-zero task capability from Mentor Graphics
  - Voted in
  - Write-up forwarded for inclusion in LRM
- Extension for a VPI Reader from Novas
  - Voted in
  - Write-up forwarded for inclusion in LRM
- Extension for VPI to support interface to SystemVerilog
  - Voted in
  - Write-up forwarded for inclusion in LRM
- Errata lists for the following items have been generated
  - assertion VPI
  - VPI extension
  - DPI

- **LRM**

- Focus on review and consistency checks across other committees
- Six different groups were formed for review of the LRM and their comments were merged into LRM.