

Merged Keywords

P1800-2009 Keywords	AMS 2.3.1 Keywords
	above
	abs
	absdelay
	abstol
accept_on	
	access
	acos
	acosh
	ac_stim
alias	
	aliasparam
always	
always_comb	
always_ff	
always_latch	
	analog
	analysis
and	
	asin
	asinh
assert	
assign	
assume	
	atan
	atan2
	atanh
automatic	
before	
begin	
bind	
bins	
binsof	
bit	
	branch
break	
buf	
bufif0	
bufif1	
byte	
case	
casex	
casez	
	ceil
cell	
chandle	
checker	
class	
clocking	
cmos	
config	
	connect
	connectmodule
	connectrules

Merged Keywords

const	
constraint	
context	
continue	
	continuous
	cos
	cosh
cover	
covergroup	
coverpoint	
cross	cross
	ddt
	ddt_nature
	ddx
deassign	
default	
defparam	
design	
disable	
	discipline
	discrete
dist	
do	
	domain
	driver_update
edge	
else	
end	
endcase	
endchecker	
endclass	
endclocking	
endconfig	
	endconnectrules
	enddiscipline
endfunction	
endgenerate	
endgroup	
endinterface	
endmodule	
	endnature
endpackage	
	endparamset
endprimitive	
endprogram	
endproperty	
endspecify	
endsequence	
endtable	
endtask	
enum	
event	
eventually	
	exclude
	exp

Merged Keywords

expect	
export	
extends	
extern	
final	
	final_step
first_match	
	flicker_noise
	floor
	flow
for	
force	
foreach	
forever	
fork	
forkjoin	
	from
function	
generate	
genvar	
global	
	ground
highz0	
highz1	
	hypot
	idt
	idtmod
	idt_nature
if	
iff	
ifnone	
ignore_bins	
illegal_bins	
implies	
import	
inmdir	
include	
	inf
initial	
	initial_step
inout	
input	
inside	
instance	
int	
integer	
interface	
intersect	
join	
join_any	
join_none	
	laplace_nd
	laplace_np
	laplace_zd
	laplace_zp

Merged Keywords

large	
let	
liblist	
library	
	limexp
	ln
local	
localparam	
	log
logic	
longint	
macromodule	
matches	
	max
medium	
	merged
	min
modport	
module	
nand	
	nature
negedge	
	net_resolution
new	
nexttime	
nmos	
	noise_table
nor	
noshowcancelled	
not	
notif0	
notif1	
null	
or	
output	
package	
packed	
parameter	
	paramset
pmos	
posedge	
	potential
	pow
primitive	
priority	
program	
property	
protected	
pull0	
pull1	
pulldown	
pullup	
pulsestyle_ondetect	
pulsestyle_onevent	
pure	

Merged Keywords

rand	
randc	
randcase	
randsequence	
rcmos	
real	
realtime	
ref	
reg	
reject_on	
release	
repeat	
	resolveto
restrict	
return	
rnmos	
rpmos	
rtran	
rtranif0	
rtranif1	
s_always	
s_eventually	
s_nexptime	
s_until	
s_until_with	
scalared	
sequence	
shortint	
shortreal	
showcancelled	
signed	
	sin
	sinh
	slew
small	
solve	
specify	
specparam	
	split
	sqrt
static	
string	string
strong	
strong0	
strong1	
struct	
super	
supply0	
supply1	
sync_accept_on	
sync_reject_on	
table	
tagged	
	tan
	tanh

Merged Keywords

task	
this	
throughout	
time	
timeprecision	
	timer
timeunit	
tran	
tranif0	
tranif1	
	transition
tri	
tri0	
tri1	
triand	
trior	
trireg	
type	
typedef	
union	
unique	
unique0	
	units
unsigned	
until	
until_with	
untyped	
use	
uwire	
var	
vectored	
virtual	
void	
wait	
wait_order	
wand	
weak	
weak0	
weak1	
while	
	white_noise
wildcard	
wire	
with	
within	
wor	
xnor	
xor	
	zi_nd
	zi_np
	zi_zd
	zi_zp