
PSL VHPI access

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Background

- PSL will be used for adding verification capabilities to VHDL
- PSL assertions may appear in VHDL source
- PSL LRM will be included by reference

VHPI PSL access extensions proposal

- VHPI extensions for PSL assertions defined
 - Integrated with current VHPI model
 - UML model generated
 - Header file extensions
- Capabilities:
 - Provide static access to PSL directives, sequences and assertions and traversal of the PSL expressions
 - Some runtime capabilities
- VHPI extension proposal for PSL assertion access donated to the IEEE on June 7, 2004

Next Milestones

- Immediate:
 - Technical review of proposal
 - Incorporation in the LRM section for VHPI
- Future milestone: coverage access