

Errata: CS4341 Data Sheet Correction

(Reference CS4341 Data Sheet revision DS298PP2 AUG '99)

The following is a correction to the CS4341 Data Sheet revision DS298PP2 AUG'99. The changes shown in this errata will be corrected in the next revision of the data sheet.

- The following table shows the correct 3 Volt performance numbers.

Parameter	Symbol	Base-rate Mode			High-Rate Mode			Unit
		Min	Typ	Max	Min	Typ	Max	
Dynamic Performance for VA = 3 V								
Specified Temperature Range	T _A	-10	-	70	-10	-	70	°C
Dynamic Range	(Note 1)							
18 to 24-Bit	unweighted	89	94	-	87	92	-	dB
	A-Weighted	92	97	-	91	96	-	dB
16-Bit	unweighted	-	93	-	-	91	-	dB
	A-Weighted	-	96	-	-	96	-	dB
Total Harmonic Distortion + Noise	(Note 1)	THD+N						
18 to 24-Bit	0 dB	-	-94	-89	-	-92	-87	dB
	-20 dB	-	-74	-69	-	-72	-67	dB
	-60 dB	-	-34	-29	-	-32	-27	dB
16-Bit	0 dB	-	-93	-	-	-91	-	dB
	-20 dB	-	-73	-	-	-71	-	dB
	-60 dB	-	-33	-	-	-31	-	dB

Notes: 1. One-half LSB of triangular PDF dither is added to data.

- The following settings of the ATAPI bits are incorrectly described in the data sheet. The correct functionality is shown below.

ATAPI4	ATAPI3	ATAPI2	ATAPI1	ATAPI0	AOUTA	AOUTB
1	0	0	1	1	MUTE	[bL/2]
1	0	1	1	1	aR	[(aR+bL)/2]
1	1	1	0	0	[aL/2]	MUTE

Contacting Cirrus Logic Support:

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