Errata: CS61584A Revision D

(Reference CS61584A Data Sheet revision DS261PP4 dated JAN '99)

This document is an errata for Revision D of the CS61584A device, and describes the differences between Revision D and the referenced data sheet. Revision E is expected to fully meet the data sheet specifications.

1. Receiver Sensitivity and Loss of Signal (LOS)

- Rev D: The receiver sensitivity and LOS thresholds are currently as follows:

	Sensitivity	LOS
T1	-11.5dB	-14dB
E1	-9.2dB	-11dB

- Rev E: The receiver sensitivity and LOS thresholds will be as follows:

	Sensitivity	LOS
T1	-12dB	-20dB
E1	-12dB	-20dB

2. Quartz Crystal Loading Issue

- Rev D: The load presented to the quartz crystal, across the pins 1XCLK/XTALOUT (pins 28/27, TQFP) is around 31pF instead of 19pF. This can make the quartz crystal run at frequencies up to 1.5kHz lower than the nominal 12352/16384kHz frequencies. This results in the device generating frequencies such as RCLK (in the absence of input data and REFCLK/oscillator), or the TAOS signal (blue alarm/all ones), to be up to 160Hz lower than the expected T1/E1 frequencies. A work around is to add series capacitance to the quartz crystal, thus reducing the total loading; or using a crystal which runs at its nominal frequency with a 31pF load, instead of 19pF. Please note that if the REFCLK or an oscillator is being used then this problem does not manifest itself.

- Rev E: The issue persists in rev E.

The device revision is the letter before the 4 digit assembly date code in the second line of the package marking.

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