Errata: CS43L43 Data Sheet Correction

(Reference CS43L43 Data Sheet revision DS479PP1 dated Feb '00)

The following is a correction to the CS43L43 Data Sheet revision DS479PP1, FEB'00. The changes in this errata will be corrected in the next revision of the data sheet.

The Control Port Enable bit, CP_EN, was inadvertently omitted from the register description section in the data sheet. Register 01h should include the CP_EN in bit location 0 as follows:

POWER AND MUTING CONTROL (ADDRESS 01H)

7	6	5	4	3	2	1	**0**
AMUTE	SCZ1	SCZ0	POR	Reserved	Reserved	PDN	**CP_EN**
1	1	0	1	0	0	1	**0**

CONTROL PORT ENABLE (CP_EN)

Default – 0
0 – Disabled
1 – Enabled

Function:

The Control Port will become active and reset to the default settings when this function is enabled.

If there are any questions concerning this information,
Please contact Applications Engineering: (512) 445-7222