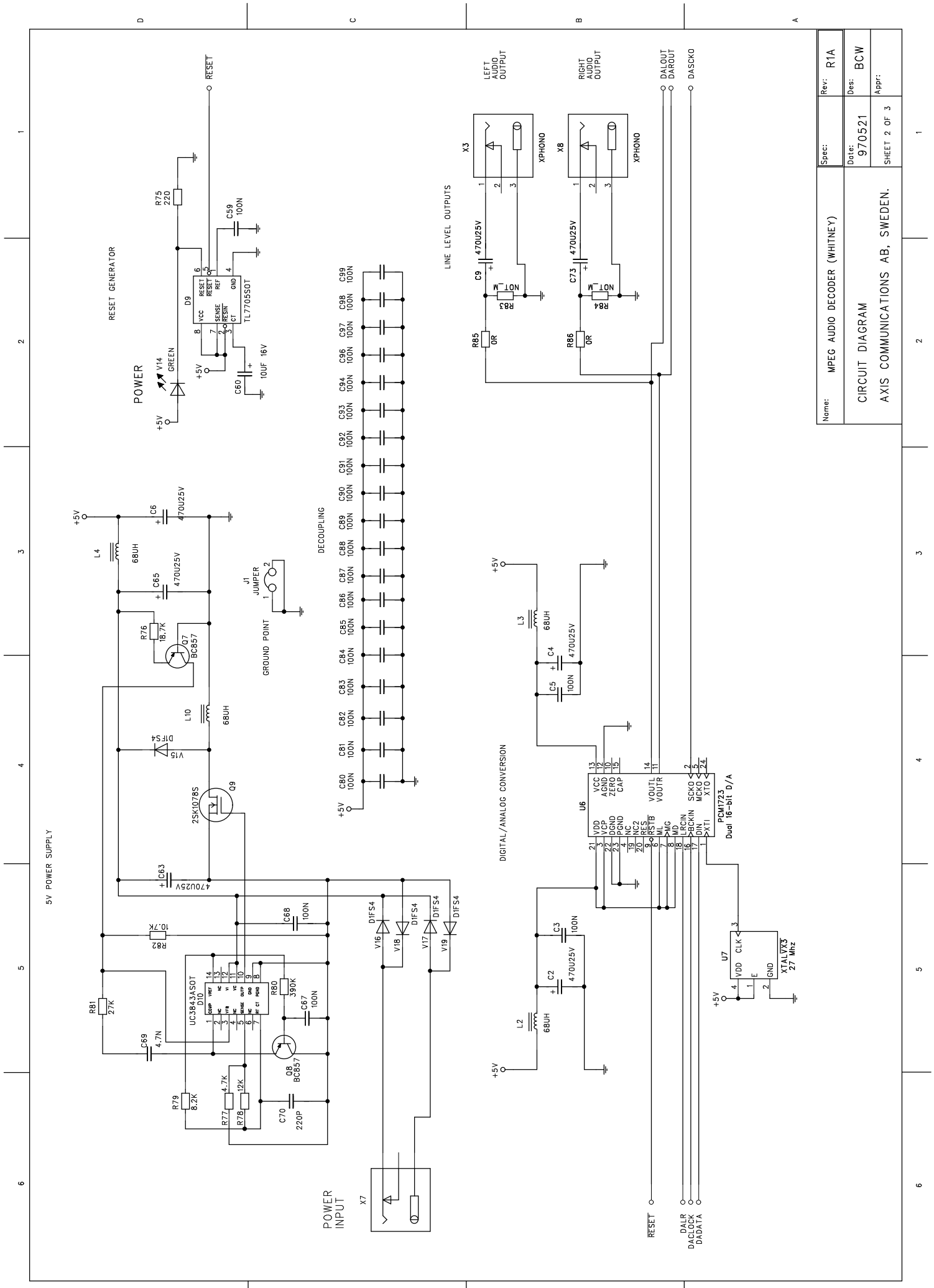
RESET GENERATOR
POWER
V14 GREEN
R75 220
C59 100N
D9 TL7705SOT
C60 10UF 16V
RESET
5V POWER SUPPLY
L4 68UH
C6 470U25V
C65 470U25V
R76 18.7K
Q7 BC857
L10 68UH
D1FS4 V15
Q9 2SK1078S
C63 470U25V
R82 10.7K
R81 27K
C69 4.7N
UC3843ASOT D10
R80 390K
C67 100N
C68 100N
Q8 BC857
R79 8.2K
R77 4.7K
R78 12K
C70 220P
J1 JUMPER
GROUND POINT
DECOUPLING
C80 100N
C81 100N
C82 100N
C83 100N
C84 100N
C85 100N
C86 100N
C87 100N
C88 100N
C89 100N
C90 100N
C91 100N
C92 100N
C93 100N
C94 100N
C96 100N
C97 100N
C98 100N
C99 100N
D1FS4 V16
D1FS4 V18
D1FS4 V17
D1FS4 V19
POWER INPUT
X7
LINE LEVEL OUTPUTS
LEFT AUDIO OUTPUT
X3 XPHONO
C9 470U25V
R83 NOT_M
R85 0R
RIGHT AUDIO OUTPUT
X8 XPHONO
C73 470U25V
R84 NOT_M
R86 0R
DALOUT
DAROUT
DASCKO
DIGITAL/ANALOG CONVERSION
L3 68UH
C4 470U25V
C5 100N
U6 PCM1723 Dual 16-bit D/A
C3 100N
C2 470U25V
L2 68UH
U7 XTALVX3 27 Mhz
RESET
DALR
DACLOCK
DADATA
Name: MPEG AUDIO DECODER (WHITNEY)
CIRCUIT DIAGRAM
AXIS COMMUNICATIONS AB, SWEDEN.
Spec:
Rev: R1A
Date: 970521
Des: BCW
Appr:
SHEET 2 OF 3