The Xilinx Customer Education Group has developed two new courses: a one-day Foundation Series schematic entry course and a one-day synthesis course based on Synopsys. The Foundation Series schematic-entry course offers a combination of lecture and labs to enhance the learning experience. The course syllabus includes the Schematic Editor, HDL Editor, State Machine Bubble Diagrammer, Simulator, and Project Manager. To reinforce the lecture materials, the course includes four lab exercises, plus an optional lab presenting the advanced features of the simulator.

The one-day Synopsys-based synthesis course focuses on the implementation of an FPGA from a synthesized design. The course is designed for experienced Xilinx and Synopsys users familiar with the Alliance Series software and the FPGA Compiler. Topics included in this course include: Setting up for Synthesis, Generation of Efficient HDL code, and the Xilinx and Synopsys methodologies. Three labs are included to further enhance the synthesis learning experience.

We offer these new courses at the Xilinx headquarters office in San Jose, Calif., and our distributors worldwide will also be offering these same courses. You can find the exact dates for the scheduled distributor courses in your area by checking with your distributor or by browsing the educational information on WebLINX (www.xilinx.com). ◆

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