

Integrating XC9500 ISP Capabilities With Manufacturing Test on the Teradyne Z1800

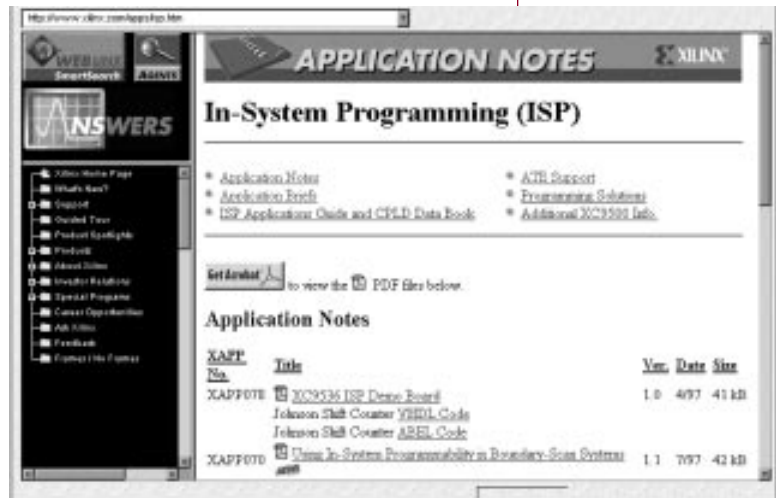
In-system programming (ISP) allows you to program and re-program devices that are already soldered on a system board. ISP streamlines manufacturing flows, allows you to update and reconfigure remote systems, and makes prototyping much easier. The XC9500 family integrates ISP functionality through the IEEE 1149.1 (JTAG) test access port without requiring any externally applied voltages greater than 5V. This allows JTAG-compatible automatic test equipment, such as the Teradyne Z1800, to program XC9500 devices.

Z1800 Configuration and Fixturing

The Teradyne Board Test System performs ISP as an integrated part of the manufacturing test process. In order to integrate programming into the system test flow, you need:

- A Teradyne Z1800 tester with the digital functional processor board running the F1 software.
- The Xilinx EZTag or JTAGProgrammer software.
- The zip file containing the Teradyne SVFP translator, C files, and software libraries (downloaded from WebLINUX).

- A C compiler capable of producing a16-bit DOS executable for the Z1800. The computers provided with Teradyne board test systems support Microsoft Visual C++ and Borland Turbo C++ version 3.0 compilers.



Availability

The svfp translator, C files, libraries, and accompanying documentation are available on WebLINUX (www.xilinx.com), the Xilinx Web site. Point your browser to <http://www.xilinx.com/apps/isp.htm> and select the topic titled *Programming Xilinx XC9500 on a Teradyne Z1800 with DFP - EZTag Version*.

Xilinx now offers you full JTAG/ISP support for the top three ATE manufacturers: Teradyne, HP, and GenRad. The necessary software and documentation for all three ATE platforms is available free of charge from WebLINUX.

With complete ATE support and the industry's most extensive JTAG capability, Xilinx is the ISP CPLD vendor of choice. ♦

Find it on the Web at:
<http://www.xilinx.com/apps/isp.htm>

