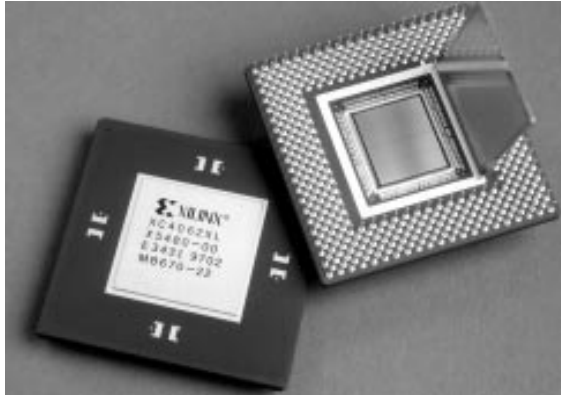


XC4062XL Debuts: *Xilinx Extends High-Density FPGA Leadership*



10

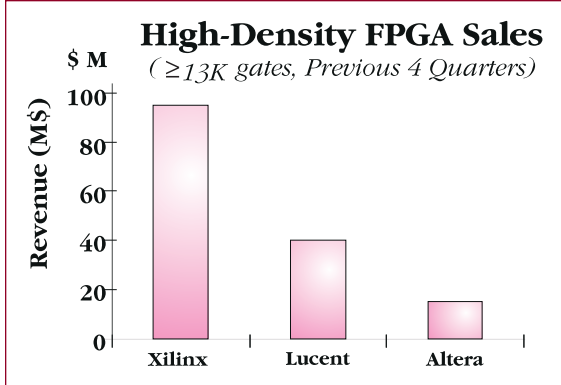
In January, Xilinx began sampling the XC4062XL FPGA device, currently the world's highest-capacity FPGA. Xilinx has shipped more than \$90 million worth of high-density FPGAs over the past 12 months, more than all competitors combined.

The XC4062XL device, the largest available member of the XC4000XL family, is manufactured on an advanced 0.35 μ process and operates at 3.3 volts. It contains 2,304 CLBs (5,472 logic cells).

Logic cells are fast becoming the industry-standard metric for comparing densities of SRAM-based FPGAs. A logic cell is defined as the combination of a 4-input look-up table (LUT) and a dedicated register that reside in the same block, such that the output of the LUT can be the data input to the register.

The majority of FPGAs in use today are SRAM-based FPGAs whose logic blocks are based on a combination of memory look-up tables (LUTs) and dedicated registers. Thus, logic cells are a more direct and objective capacity metric than gate counts. Each XC4000 CLB, with two 4-input LUTs, one 3-input LUT, and two registers, is equivalent to 2.375 logic cells.

The XC4062XL is available in 560-pin ball grid array (BGA), 475-pin pin grid array (PGA) and 240-pin high performance quad flat pack (HQFP) packages. ♦

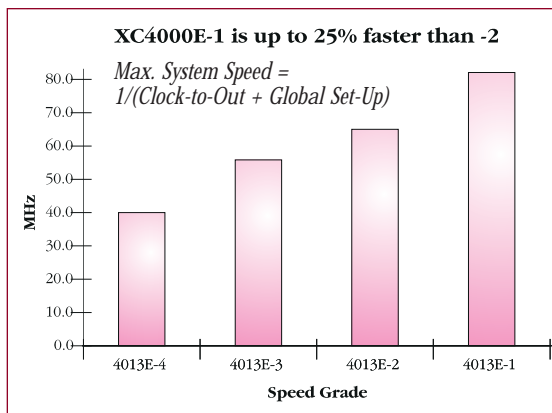


Faster XC4000E-1 FPGAs Support High-Performance Applications

Once again, continuing process improvements have led to the release of a new, faster speed grade for the XC4000E FPGA family. Devices with the new -1 speed grade are expected to start sampling in March and enter full production in June.

Based on an optimized 0.5 μ three-layer-metal process, the new -1 rated devices support typical system clock speeds

in the 80 MHz range — about 20% higher performance than the XC4000E-2 FPGAs. The



XC4000E-1 devices are applicable to a wide range of high-density, high-performance applications, such as PCI bus interfaces, fast ATM switches and video processing.

For pricing and availability information, please contact your local Xilinx sales representative. ♦