

One-Day VHDL Seminar Now Available

Xilinx is sponsoring a series of one-day introductory VHDL seminars conducted by qualified instructors from Esperan Inc. Esperan specializes in technical training and has educated thousands of VHDL users since 1992. This one-day course, a must for new users, emphasizes the basics of the VHDL language.

A full day of lecture and hands-on labs, the seminar uses the VHDL tool offered by Xilinx in the Foundation Series™. The tuition fee is \$99. Feedback from participants has been extremely positive; extra

seminars have been added to the schedule to meet demand.

Each attendee receives an evaluation kit of the Xilinx software. This provides ample opportunity to practice the skills and knowledge obtained at the seminar. The kit includes the Foundation Series software, an integrated tool set of schematic entry, VHDL synthesis, functional and timing simulation and design implementation tools – everything needed to create an entire design.

Xilinx, its distributors and representatives, together with Esperan, are sponsoring these seminars. Contact your local Xilinx representative to inquire about VHDL training seminars in your area.

In related news, electronics distributors Hamilton Hallmark, Marshall and Insight are in the process of creating VHDL workshops as well. In each case, Xilinx FPGA devices and the Foundation Series tools have been selected for use in the lab exercises. ♦

- VHDL SEMINAR ATTENDEES LEARN:**
- The benefits of using VHDL design to reduce time-to-market
 - VHDL's general structure and syntax
 - How to write good VHDL code that will synthesize efficiently
 - How to effectively target CPLD and FPGA architectures
 - How to implement state machines, combinatorial logic and arithmetic functions in VHDL

First Hands-On Workshop on Reconfigurable Computing

The Xilinx University Program (XUP) has had an active training schedule with nine recent workshops in four countries. XUP workshops are designed to train university instructors on the basics of using programmable logic for undergraduate courses, graduate courses and research.

In addition to the standard workshops, and in response to a growing interest in dynamically-reconfigurable logic, a unique, leading-edge workshop was held at Cornell University (Ithaca, NY).

The Cornell workshop was the first to examine both the hardware and software issues associated with dynamically-reconfigurable computing, including hands-on laboratory exercises. Participating lecturers included representatives from Xilinx, the Virginia Institute of Technology, ETZ Zurich, Cornell University, and Imperial College (UK). Mr. Nick Treddinick, an IEEE fellow, presented the keynote address.

Xilinx is committed to addressing the needs of the reconfigurable logic market, and the Xilinx University Program is committed to further academic support of this exciting area of study. Proceedings from the workshop are available to XUP participants, and further information about this workshop and future XUP workshops can be obtained on the web at www.xilinx.com/programs/univ.htm. ♦

Updated Training Courses Available

The basic Xilinx technical training courses have been updated to reflect all the features of the latest development software. XACTstep™ version 6.0.1 software is fully incorporated into the schematic-based design course. The course examines a complete design scenario for the Foundation Series software user. The updated synthesis-based design course provides VHDL/Verilog designers with an intimate working knowledge of the XACTstep v5.2.1/6.0.1 synthesis software. Both of these courses are available worldwide.

The current schedule of schematic- and synthesis-based courses, as well as course registration information, can be found at the WebLINX web site (www.xilinx.com). ♦