LogiCore™ PCI Intitiator Debuts

Xilinx recently announced the delivery of its second, 100% compliant PCI core, the LogiCore™ PCI "Master" (Initiator/ Target) module. Previously, the LogiCore PCI "Slave" (Target only) module could only perform slave functions. With the new module, a user can now complete a master interface design in which the Master module acts as a controller for the PCI bus. The implementation of critical paths in the core are pre-defined to ensure PCI compliance. As a result, designers can reduce design risk and cut development time by at least nine months.

The new -2 speed grade for the XC4013E FPGA enables the delivery of the PCI Master module, which requires a high level of design complexity and performance *(see article about -2 speed grade on page 14)*. The LogiCore PCI modules were closely developed with beta users who have successfully implemented the design

in XC4013-2 FPGAs. These devices operate at 33 MHz and include high-speed burst synchronous FIFOs. The modules can be customized to fit individual design requirements, resulting in a cost-effective, single-chip solution.

The LogiCore PCI Master module (LC-DI-PCIM-C) for Xilinx FPGA and HardWire devices, complete with a data sheet, comprehensive user guide and PCI Systems Architecture textbook, is available for designing with Viewlogic schematics, VHDL and Verilog for $8,995. The PCI Slave module (LC-DI-PCIS-C) is available for $4,995.

For further information about the LogiCore PCI Interface or other modules in the program, please contact your local Xilinx representative or visit the LogiCore section of the WebLINX World Wide Web site. ◆

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Foundation Series Enjoys Successful Introduction

The number of Foundation Series™ users has been growing rapidly since the inaugural release of this fully integrated PLD design software package in 2Q96. User feedback has been extremely positive, especially regarding the ease-of-use resulting from the tight integration of all the design tools. There also has been high praise for the HDL Wizard, including the HDL Editor, with color coding, and the Language Assistant, with templates for easy and quick coding of VHDL and ABEL-HDL.

The Foundation Series development tools for FPGA and CPLD design include a highly integrated set of Windows-based design tools. Along with

the Xilinx XACT*step*™ implementation tools, it includes an HDL editor, synthesis compiler, schematic editor and simulator.

Xilinx has added more features to the Foundation packages in the v6.0.1 release, which is now shipping. Features in this release include support for the latest Xilinx device families: the XC9500 CPLDs and XC4000E FPGAs. Both device families are integrated into the easy-to-use, shrink-wrapped solution, making it simple to access and convert designs for these new families. All users who purchased Foundation v6.0 will automatically receive this new update. ◆

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