



HardWire™ Array Custom Mark Request Form

This form should be used only when more than 1 line of custom marking is requested.

Customer requests that their HardWire devices of the referenced Xilinx part number be custom marked as shown below. Customer acknowledges that if they subsequently desire to change the custom marking, they are responsible for existing inventory at the standard purchase price.

Company Name _____ Date _____

Customer Name _____ E-mail _____

Address _____ City _____ State/Province _____

Country _____ Zip _____ Tel () _____ Fax () _____

Customer Internal Part Number _____

Customer Part Description _____

Xilinx Part Number HardWire Array _____

TOP VIEW

Location of Pin 1
Identifier for PC
Packages

Diagram showing the marking locations for the HardWire Array. The diagram is a 10x4 grid of boxes. The first three rows are empty. The fourth row contains 'H H H H - x x x x x'. The fifth row contains 'y y y y y'. A circle at the top center indicates the location of Pin 1 for PC Packages. A circle at the bottom left indicates the location of Pin 1 for PQ/TQ/VQ Packages.

Location of Pin 1
Identifier for PC
PQ/TQ/VQ
Packages

Notes:

1. A maximum 3 lines (including any desired logo or graphics). Some characters require more than 1 line.
2. Max 11 characters per line.
3. Mark is center justified.
4. Camera-ready artwork must be provided for all desired graphics or logos.
5. \$500 setup fee for custom marking.
6. Only the characters appearing on this form, and attached artwork, will appear on the package.
7. Allow 1 extra week in prototype production for Custom Mark.

Legend:

HHHH = Xilinx HPC code

xxxxx = reserved

yyyyy = Date Code ID

Customer Acceptance:

Signature _____ Date _____

Title _____

☐ CUSTOMER ARTWORK ATTACHED