



PCMCIA Card Debugger/Exerciser

May 25, 1997

Product Specification

DO

Digital Objects Corp.

Formerly Mobile Media Research, Inc.
3550 Mowry Ave., Suite 101
Fremont, CA 94538 USA
Phone: +1 800-799-6674
+1 510-795-2212
Fax: +1 510-795-2219
E-mail: sales@digitalobjects.com
URL: www.digitalobjects.com

Overview

Digital Objects Corporation's PCMCIA Card Debugger/Exerciser is designed for both hardware and software engineers in the development phase of a PCMCIA product.

Professional and Lite versions are available depending upon the needs and budget of the user. The features of each are listed below.

Lite Version Features (SW-002-LV)

- Engineering debug software for PCMCIA hosts and cards
 - Supports 82365SL-compatible based hosts
- Fully compatible Microsoft Windows-based GUI
 - Provides easy control of host functions
 - Generates PCMCIA bus cycles on the selected socket
 - Allows direct access to PCIC registers
 - Integrates all functionality necessary for PCMCIA compatibility testing in one screen
- Supports basic and necessary PCMCIA debug and testing features:
 - Register access
 - I/O and memory window set-up
 - Socket configuration access

Card Memory Address					
Start #0	FFFF	Stop #0	FFFF	Offset #0	FFFF
Start #1	FFFF	Stop #1	FFFF	Offset #1	FFFF
Start #2	FFFF	Stop #2	FFFF	Offset #2	FFFF
Start #3	FFFF	Stop #3	FFFF	Offset #3	FFFF
Start #4	FFFF	Stop #4	FFFF	Offset #4	FFFF

Control and Status Registers	
Interface Status	FF
Card Status Change	FF
Identification/Rev	FF
Pwr/Rst Div	FF
Interrupt/General Control	FF
IO Control	FF
Address Window Enable	FF
CSC Interrupt Configuration	FF

Card IO Address	
Start #0	FFFF
Stop #0	FFFF
Start #1	FFFF
Stop #1	FFFF

Read All Write All

Close

Figure 1. PCMCIA Card Debugger/Exerciser Window

Professional Version Features (SW-002-PV)

- All features of Lite version plus these debug and test features:
 - Card Configuration Register support, up to 16 registers, especially useful for multi-function cards
 - ATA card debug module (supports ATA interface)
 - Built-in CIS Parcel, with tuple browser to read the CIS
- Built-in command Macro/Interpreter
 - Supports user programmable “Macro” commands to generate system cycles for PCMCIA host/card debug
 - Up to eight user-defined command lines
 - Trace, Normal and Step options
 - Loop control capability
 - Results displayed in monitor window
- Excellent for high-volume testing
- Ideal for Host or PC-Card debug, characterization and evaluation

Available Support Products

Support products available from Digital Objects (further details provided in this product description)

- PCMCIA Prototyping Card
- CIS Generator 1.2 software

Ordering Information

To purchase or make further inquiries about this or other Digital Objects products, contact Digital Objects directly.

Related Information

Xilinx Programmable Logic

For information on Xilinx programmable logic or development system software, contact your local Xilinx sales office, or:

Xilinx, Inc.
2100 Logic Drive
San Jose, CA 95124
Phone: +1 408-559-7778
Fax: +1 408-559-7114
URL: www.xilinx.com

For general Xilinx literature, contact:

Phone: +1 800-231-3386 (inside the US)
+1 408-879-5017 (outside the US)
E-mail: literature@xilinx.com

For AllianceCORE™ specific information, contact:

Phone: +1 408-879-5381
E-mail: alliancecore@xilinx.com
URL: www.xilinx.com/products/logiccore/alliance/tblpart.htm
