XILINX®

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XILINX ANNOUNCES AGGRESSIVE PRICE REDUCTION
FOR INDUSTRY'S MOST COST–EFFECTIVE FPGA FAMILY

SAN JOSE, Calif., May 27, 1996—Xilinx, Inc. (NASDAQ:XLNX), the world's largest supplier of programmable logic, today announced plans to cut the prices of its XC5200 family of field programmable gate arrays (FPGAs) up to 53 percent effective now.

Through the use of a new manufacturing process, the XC5200 family, now the lowest price–per–gate FPGA product available, will also deliver new devices with higher performance. The advances come from the migration of the XC5200 devices from the 0.6 micron to 0.5 micron triple layer metal (TLM) CMOS process. This results in faster speed grades for all existing XC5200 devices.

Xilinx is also adding low voltage and lower power XC5200L devices, in 3.3V versions, to the family.

"The innovative XC5200 architecture, with segmented routing, scales better than competitive architectures to newer process technologies, offering better performance and the industry's smallest die size for FPGAs," said Chuck Fox, vice president marketing. "For example, the XC5210 device costs up to 45 percent less than comparable competitive SRAM architectures. Moreover, the 1997 pricing is a crossover point where FPGAs are truly price competitive in the gate array replacement market."

Projected High Volume Pricing for XC5200 Family

Device & usable gates	Old 96 High Volume	End 96 High Volume	2H97 High Volume	Total Reduction
XC5202 (84-pin PLCC pkg) 2,000 - 3,000 gates	$9	$5	$4.50	50%
XC5204 (84-pin PLCC pkg) 4,000 - 6,000 gates	$15	$10	$8	47%
XC5206 (84-pin PLCC pkg) 6,000 - 10,000 gates	$25	$17	$12	52%
XC5210 (84-pin PLCC pkg) 10,000 - 16,000 gates	$38	$27	$18	53%
XC5215 (160-pin PQFP pkg) 15,000 - 23,000 gates	$68	$47	$33	51%

In 100-unit quantities, effective July 1996, the XC5202 and the XC5204 devices (84-pin PLCC package) are $11 and $20 respectively—a reduction of up to 30 percent within one quarter.

Pricing for the new faster speed grade devices begins at $7.80 for the XC5202–4 device (84-pin PLCC package) in high-volume quantities. The 84-pin PLCC package for the XC5202L device begins at $7.30 in high volume quantities. Xilinx will begin production shipments of the XC5200–4 device in June. All device/package combinations, both 5V and 3.3V versions, will be shipping by the end of 1996.

About the XC5200 Family

The XC5200 family, including the new 3.3V versions, spans 2,000 to 23,000 usable gates and is comprised of five devices in over 100 device/package/speed combinations. Members of the family are currently shipping to users for high volume applications such as high-speed Asynchronous Transfer Mode (ATM), telecom, and networking. All the new members of the XC5200 family are

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supported by both the newest versions of the Xilinx XACT*step* Foundation Series and XACT*step* Alliance Series design and implementation software.

Founded in 1984, Xilinx is the world's largest supplier of programmable logic solutions producing industry–leading device architectures and world class design software. Headquartered in San Jose, Calif., the company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer, peripheral, telecommunications, networking, industrial control, instrumentation, and high-reliability/military markets.

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To the extent that matters covered in this release are forward looking statements, they involve risks and uncertainties, including timely availability and acceptance of new products, business conditions, the growth of the programmable logic market, and other risks that are described in the company's SEC filings, including the Form 10-Q for the quarter ended March 30, 1996.

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