✕ XILINX ®

Spartan-II Family I/V Curves for Various Output Options

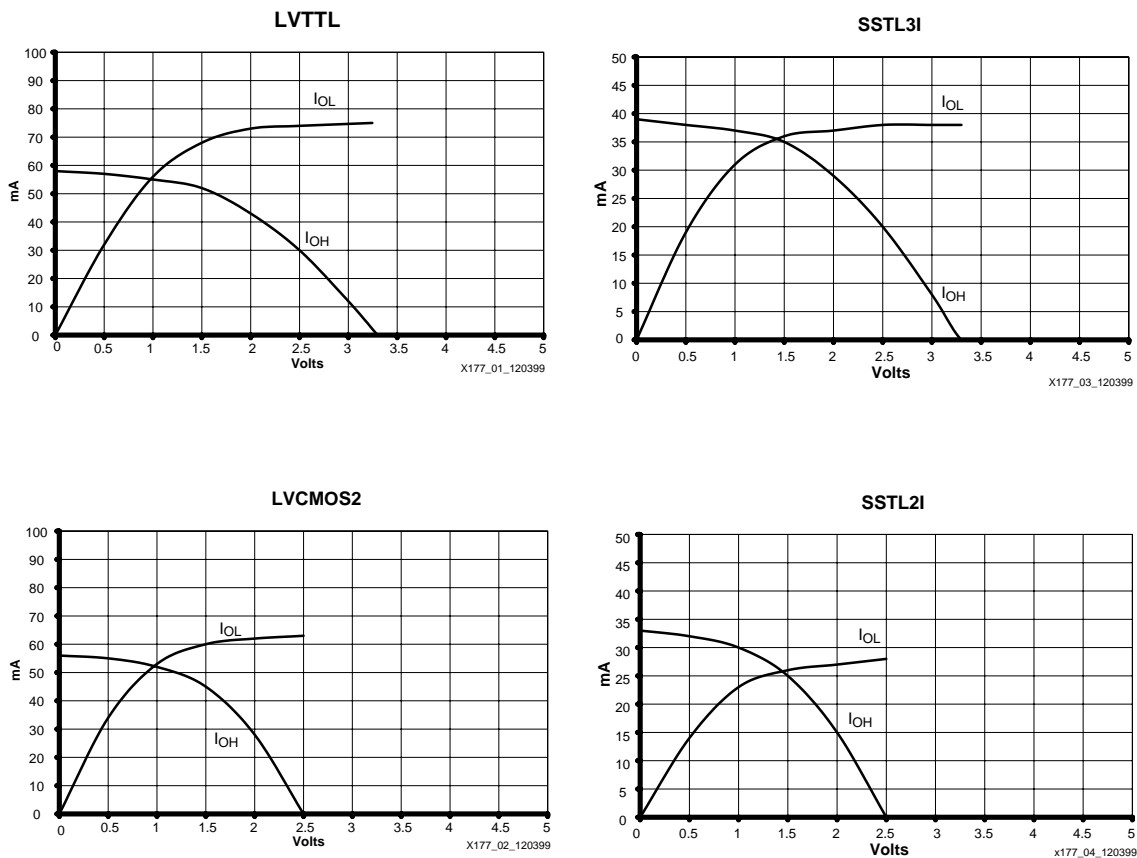XAPP177 (v0.9) December 3, 1999   Advance Information Application Note
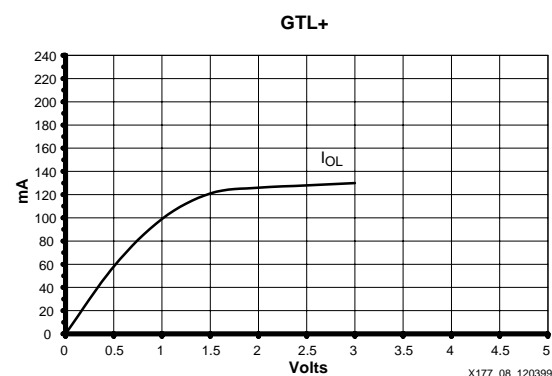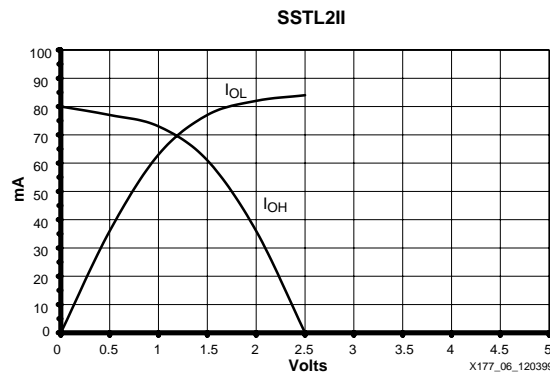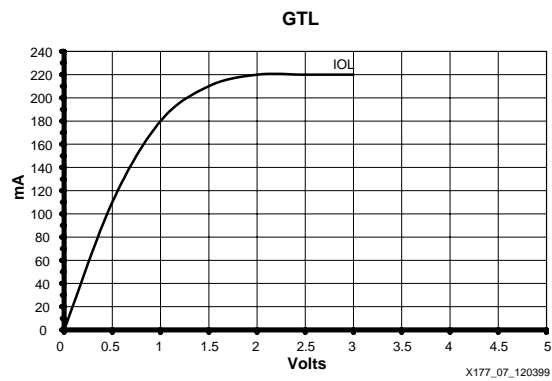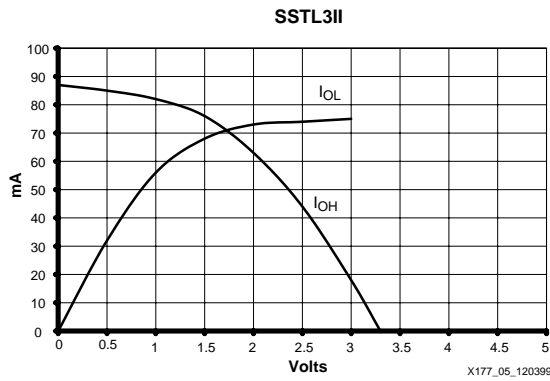
Summary

These typical curves describe the output sink and source current for average processing, nominal supply voltage and room temperature for the Spartan™-II family of FPGAs. These curves are graphical representations of IBIS models, which are traditionally used for system and board-level simulation. The Xilinx IBIS files can be found at: http://www.support.xilinx.com/support/sw_ibis.htm.

Note: The Xilinx IBIS files also list the extremes of the processing, temperature and voltage conditions as "min" and "max" (delay). Some tools rename these values as "fast-strong" and "slow-weak". This has nothing to do with the FAST or SLOW slew-rate configuration options, invoked as attributes for each output pin (the default is SLOW).

These slew-rate options are covered in separate IBIS files, with a trailing "**s**" for SLOW slew rate, and **"f"** for FAST slew rate (nly applies to LVTTL).

Spartan-II Family

LVTTL — X177_01_120399

SSTL3I — X177_03_120399

LVCMOS2 — X177_02_120399

SSTL2I — x177_04_120399

SSTL3II

I_OL

I_OH

mA

Volts

X177_05_120399

GTL

IOL

mA

Volts

X177_07_120399

SSTL2II

I_OL

I_OH

mA

Volts

X177_06_120399

GTL+

I_OL

mA

Volts

X177_08_120399

Revision History

Date	Version #	Revision
12/03/99	0.9	Advance Information Release.