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FOR IMMEDIATE RELEASE

XILINX SHIPS THE REAL 64/66 PCI,

INDUSTRY'S FIRST GENERAL-PURPOSE 64-BIT, 66 MHZ PCI SOLUTION

Virtex FPGA family meets demand for fastest available PCI compliance, flexibility and performance - before standard chips, ASICs and specialized FPGAs

SAN JOSE, Calif. – March 22, 1999 – In a move that brings programmable logic to the forefront of high-performance system level integration, Xilinx, Inc. (NASDAQ:XLNX) today announced the immediate availability of The Real 64/66 PCI™ solution. The Real 64/66 PCI core is the first complete solution that enables customers to design fully compliant yet flexible single-chip 64-bit, 66 MHz PCI bus interface systems. In addition to the new software core, Xilinx also announced a faster Virtex FPGA, the –6 speed grade, for 64-bit, 66 MHz PCI designs. The Real 64/66 PCI from Xilinx:

- Is available as a commercial product today
- Offers full compliance with the v2.2 PCI bus interface specification
- Provides 64-bit, 66 MHz performance throughout the complete design
- Gives customers the flexibility to build a single-chip design using standard FPGAs

"Xilinx PCI solutions provided us a great deal of latitude to customize our advanced networking and communications products," said Jennifer Hergert, engineering manager at Cisco Systems, San Jose, Calif. "The predefined and fully compliant Xilinx PCI core has both provided flexible solutions for our designs and aided Cisco in getting our routers to market."

"With the Real-PCI 64/66 products from Xilinx, we were able to implement a fully compliant PCI interface in our new Mx2/PCI product family plus other functions such as direct memory access (DMA), four dual-port FIFOs, and 200,000 gates of our own unique design in a single device," said John Beck, principal engineer at DOME imaging systems, Inc., Waltham, Mass.

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The Dome Mx2/PCI is the first in a new family of high resolution display controllers for the medical imaging market that can handle transfers of over 500 MBytes/second from the host. "After evaluating different solutions in the market, we found that only Xilinx could meet the demanding requirements for full 66 MHz PCI compliance."

"The Real 64/66 PCI represents the first time that an FPGA supplier has delivered a general purpose solution before manufacturers of standard chip-sets," said Wim Roelandts, Xilinx president and CEO. "This is a significant milestone that underscores the inherent benefits of standard FPGAs produced with the most advanced silicon processes. More important, The Real 64/66 PCI solution allows designers to integrate very high-performance, high-density 66 MHz PCI systems tailored to their specific needs."

Real availability

More than a dozen Xilinx customers have been actively engaged in a beta program since September using The Real 64/66 PCI core to design high-performance applications such as Gigabit Ethernet, ATM and Fiber Channel adapters, DSP and imaging boards, disk drive arrays and high-end printer interfaces. Beta customers include Ascend Communications, Westford, Mass.; Cisco Systems, San Jose, Calif.; Dome imaging systems; and Kodak, Rochester, N.Y.

Real compliance

The Real 64/66 PCI solution provides true timing, functional and electrical PCI v2.2 compliance by using Xilinx Smart-IP technology to guarantee critical minimum, maximum and hold timing required for a true zero wait-state burst operation at 66 MHz. Compliance is verified through hardware testing, device characterization and regression testing using an internal test bench that simulates more than six million unique combinations of PCI transactions.

"Xilinx released its first PCI core more than four years ago, and our PCI products to date have been used in more than 1,000 customer designs," said Rich Sevcik, senior vice president of software, cores and support at Xilinx. "This rich PCI heritage has given Xilinx experience with PCI that is unmatched in the industry. It has allowed us to develop the design and verification processes necessary to build and support high-quality PCI products. The Real PCI 64/66 core, which can be downloaded from the Xilinx Web site, reinforces our on-going Silicon Xpresso initiative to use the Internet to increase the productivity of designers."

Real performance

The Virtex FPGAs are manufactured on a state-of-the-art 0.22 micron process that meets all timing requirements for 64-bit, 66 MHz performance, up to the theoretical maximum throughput of 528 Mbytes per second.

Xilinx 64/66 PCI Core

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The Real 64/66 PCI solution is fully verified by Xilinx for the Virtex XCV300-6 BG432 and XCV1000-6 FG680 devices, which offer densities of 300,000 and one million system gates, respectively.

Real flexibility

Implemented in Xilinx standard Virtex FPGAs, The Real 64/66 PCI solution allows customers to benefit from real flexibility provided only by standard, off-the-shelf FPGAs. For example, a designer can choose Virtex device size and package type, customize the PCI feature set, and adapt the design later to future changes in the PCI standard or new PCI requirements. Both Synopsys and Synplicity support The Real 64/66 PCI core in their design flows.

"The Real 64/66 PCI solution will allow FPGA customers to implement the logic for high performance bus designs using the familiar Synopsys FPGA design flow," said Jay Michlin, vice president and general manager of the FPGA business unit at Synopsys. "Our FPGA Compiler II and FPGA Express synthesis tools aid designers incorporating this new state-of-the-art intellectual property for FPGAs into very demanding design situations."

Xilinx PCI Training

To further complete the Xilinx PCI solution, Xilinx will offer a two-day PCI course beginning in May for customers who are planning PCI systems. The course will give an introduction to the PCI standard, cover configuration and integration of The Real 64/66 PCI core, system integration, verification and debug. In addition to Xilinx classes, PCI design services are available from a number of partners in the worldwide Xilinx XPERTS design consulting program.

Pricing and Availability

The Real 64/66 PCI core for the Virtex XCV300 BG432 device and the Virtex -6 speed grade FPGAs are available now from Xilinx. The Real 64/66 PCI product is priced at \$14,995.

Xilinx is the leading innovator of complete programmable logic solutions, including advanced integrated circuits, software design tools, predefined system functions delivered as cores, and unparalleled field engineering support. Founded in 1984 and headquartered in San Jose, Calif., Xilinx invented the field programmable gate array (FPGA) and commands more than half of the world market for these devices today. Xilinx solutions enable customers to reduce significantly the time required to develop products for the computer, peripheral, telecommunications, networking, industrial control, instrumentation, high-reliability/military, and consumer markets. For more information, visit the Xilinx web site at www.xilinx.com.

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