What's New in TRADING...

Xilinx announces a new Advanced FPGA class.

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Xilinx Customer Education offers a number of public classes on-site training courses and on-line E-Learning modules to help you quickly learn the latest development tools. Our public courses are held at various locations around the world, and our E-Learning modules are available online, via the Web. On-site classes are scheduled on request.

Online E-Learning Modules

Each E-Learning module is a one-hour session that focuses on specific product solutions, and you have the ability to ask questions of the instructor, get answers, and collaborate with other students. Our E-Learning modules are available now as live, on-line sessions ($100.00 USD per module) and will soon be available in recorded versions ($70.00 USD per module). New modules are added frequently, so check our website to see the latest sessions and schedules.

New On-site Advanced FPGA Class

Our new on-site, two-day Advanced FPGA class is designed to help you:

- Get higher performance from your design.
- Optimize your code for the Virtex architecture.
- Take advantage of the more advanced capabilities of the implementation tools, such as Multi-pass Place and Route, and re-entrant routing.

Course topics include:

- Targeting Virtex-specific features with Synopsys FPGA Express and Synplicity Synplify.
- Using advanced timing constraints.
- Using the FPGA Editor.
- Using Xflow.
- Creating Virtex memory applications.

Class size is limited, so enroll early. To learn more about our classes or to register you can contact the registrar at 877-XLX-CLASS (877-959-2527), or go to: http://www.support.xilinx.com /support/training/training.htm.

Advanced FPGA Class Schedule		
May 9 & 10	San Jose, California	
June 8 & 9	Boston, Mass	
June 13 & 14	San Jose, California	
July 12 & 13	Boston, Mass	
July 26 & 27	San Jose, California	
Aug 9 & 10	San Jose, California	
August 24 & 25	Boston, Mass	
Other Classes		
June 6	Alliance 3.1 Update	Boston
June 7	Practical Design	Boston
June 20-22	Intro to Verilog	San Jose
June 27	Foundation ISE Design Entry	San Jose
June 28-30	FPGA Design-ASIC User	San Jose
July 10	Alliance 3.1 Update	Boston
July 11	Practical Design	Boston
July 18-20	Introduction to VHDL	San Jose
July 18	PCI Basics	Boston
July 19-20	Designing w/PCI	Boston