

	Feature	ISE WebPACK	ISE BaseX	ISE Foundation	ISE Alliance
Device Support	Platform	PC	PC	PC/UNIX	PC/UNIX
	Virtex™ Series	Up to 300K (Virtex-E and Virtex-II)	Up to 300K	ALL	ALL
	Spartan™ II/IE Families	ALL	ALL	ALL	ALL
	Spartan™ /XL Families	No	Implementation Only*	Implementation Only*	ALL
	XC4000™ Series	No	Implementation Only*	Implementation Only*	ALL
	CoolRunner XPLA3 / CoolRunner™-II Families	ALL	ALL	ALL	ALL
	XC9500™ Series	ALL	ALL	ALL	ALL
Design Planning	Modular Design	No	Sold as an Option	Sold as an Option	Sold as an Option
	Educational Services	Yes	Yes	Yes	Yes
	Design Services	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
	Support Services	Web Only	Yes	Yes	Yes
Design Entry	Schematic Editor (Gate & Block Level HDL)	Yes	Yes	PC Only	No
	HDL Editor	Yes	Yes	Yes	Yes
	State Diagram Editor	Yes	Yes	PC Only	No
	CORE Generator	No	Yes	Yes	Yes
	Xilinx System Generator for Simulink	No	Sold as an Option	Sold as an Option	Sold as an Option
Synthesis	Xilinx Synthesis Technology (XST)	Yes	Yes	Yes	No
	Synplicity Synplify/Pro	No	Integration Kit	Integration Kit (PC Only)	Integration Kit (PC Only)
	Leonardo Spectrum	Integration Kit	Integration Kit	Integration Kit	Integration Kit
	Synopsys FPGA Compiler II	EDIF Only	EDIF Only	EDIF Only	EDIF Only
	ABEL	CPLD	CPLD	CPLD (PC Only)	No
Implementation Tools	iMPACT	Yes	Yes	Yes	Yes
	FloorPlanner	Yes	Yes	Yes	Yes
	Xilinx Constraints Editor	Yes	Yes	Yes	Yes
	Timing Driven Place & Route	Yes	Yes	Yes	Yes
	Timing Improvement Wizard	No	Yes	Yes	Yes
Board Level Integration	IBIS Models	Yes	Yes	Yes	Yes
	STAMP Models	Yes	Yes	Yes	Yes
	LMG SmartModels	Yes	Yes	Yes	Yes
	HSPICE Models**	Yes	Yes	Yes	Yes
Verification	HDL Benchertm	Yes	Yes	PC Only	No
	ModelSim Xilinx Edition (MXE II)	ModelSim XE II Starter***	ModelSim XE II Starter***	ModelSim XE II Starter***	ModelSim XE II Starter***
	Static Timing Analyzer	Yes	Yes	Yes	Yes
	ChipScope	No	Sold as an Option	Sold as an Option	Sold as an Option
	FPGA Editor with Probe	No	Yes	Yes	Yes
	ChipViewer	Yes	Yes	Yes	Yes
	XPower (Power Analysis)	Yes	Yes	Yes	Yes
	3rd Party Simulator Support	Yes	Yes	Yes	Yes
IP/CORE	For more information on the complete list of Xilinx IP products, visit the Xilinx IP Center at http://www.xilinx.com/ipcenter				

* For Spartan, SpartanXL, and 4K device families, ISE supports the implementation by reading in gate-level EDIF netlist.

** HSPICE Models available at the Xilinx Design Tools Center at www.xilinx.com/ise.

*** MXE II supports the simulation of designs up to 1 million system gates and is sold as an option.