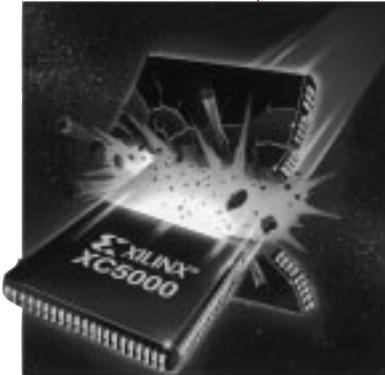


The XC5200 Family – *Now 30% Faster*

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The XC5200 family is now 30 percent faster with the introduction of the new XC5200-3 and XC5200-4 speed grades. Xilinx introduced the XC5200 family in 1995 as the first FPGAs optimized for the high-volume, low-cost, consumer applications that once required custom gate arrays. Now, due to wide acceptance, the XC5200 is the industry's fastest growing FPGA family, and the third-largest in unit shipments, with more than three million units shipped.



This family offers five devices ranging from 256 to 1,936 logic cells, allowing you to replace gate arrays using up to 20,000 logic gates. The devices come in 17 different packages, in both commercial and industrial grades, including our new VQ64 package that provides the most space-efficient footprint for consumer, hand-held, and other small form factor applications. And, footprint compatibil-

ity allows you to easily move to the XC4000 family for higher speed, higher density, on-chip RAM, or 3-volt operation.

The new XC5200 data sheet, with the latest specifications and an expanded architectural description, is available in our new *1998 Data Book* and on WebLINX (www.xilinx.com), the Xilinx site on the World Wide Web. The XC5200-3 speed files with 30 percent higher performance are available from the "file download" area of WebLINX.

All XC5200 devices are fully supported by the Xilinx Foundation and Alliance Series software solutions. The low-cost base system has been extended to add support for the XC5206 and XC5210 devices, in addition to the XC5202 and XC5204 devices. The combination of powerful, easy-to-use software and a low-cost programmable architecture, gives you the shortest time-to-market, eliminating the need for gate arrays, even in high-volume consumer applications. ♦