



- NOTES: 1. P1 is a 96 pin male connector, 3 rows (A,B,C) of 32 pins.
 2. S1 is a 144 pin CSP socket.
 3. M1 are connection pads for an autohandler.
 4. Board ID is B6 hex, 1011 0110 bin.
 5. Signal names for XCR3128XL
 6. Signal names for P1-B8 and P1-B31 are for HW-130 ML7 only and were formerly PVSP.
 7. FPGA Design is modified to accept TMS on A1 or A2.

				TITLE SCHEMATIC, HW-136-CS144 ADAPTER	
				PROJECT: HW-130 ADAPTERS	
DRAWN BY Carol		DATE 5/11/00		SIZE	DRAWING NUMBER
CHECKED BY		DATE		B	0381084
ENG APPROVAL		DATE			
MFG APPROVAL		DATE			
TOLERANCES ∠ .x .xx .xxx		Unless otherwise specified, dimensions are in inches.		SCALE NONE	SHEET 1 OF 1