































	$\sim$	GA Connec	ctor I
D2000 Hu=StarWurks	1 42 A		$O\left(\begin{smallmatrix} 0&0&0&0\\0&0&0&0\\0&0&0&0\\0&0&0&0\\ \end{smallmatrix}\right)$
	1: Red out	6: Red return (ground)	11: Monitor ID 0 in
	2: Green out	7: Green return (ground)	12: Monitor ID 1 in or data from display
	3: Blue out	8: Blue return (ground)	13: Horizontal Sync
H	4: Unused	9: Unused	14: Vertical Sync
	5: Ground	10: Sync return (ground)	15: Monitor ID 3 in or data clock



	VGA	Timing 🛛
	Horizonal Dots Vertical Scan Lines Horiz. Sync Polarity A (μs) B (μs) C (μs) D (μs) F (μs)	640 480 60Hz vertical frequency NEG 31.77 Scanline time 3.77 Sync pulse length 1.89 Back porch 25.17 Active video time
	E (µS)	

	VGA	Timing 🗖
	Horizonal Dots	640
	Vertical Scan Lines	480 60Hz vertical frequency
	Horiz. Sync Polarity	NEG
	A (µs)	31.77 Scanline time
	Β (μs)	3.77 Sync pulse length
	C (µs)	1.89 Back porch
	D (µs)	25.17 Active video time
	E (µs)	0.94 Front porch
	25.17/640 = 39.33ns/j	pixel = 25.4MHz pixel clock
	VIDEO	VIDEO (next line)
	_   B	_
	AA	

	VC	GA 1	Timing 🗖
	Horizonal Dots Vertical Scan Lines Vert. Sync Polarity Vertical Frequency O (ms) P (ms) Q (ms) R (ms) S (ms)	640 480 NEG 60Hz 16.68 0.06 1.02 15.25 0.35	Total frame time Sync pulse length Back porch Active video time Front porch
	VIDEC	)	VIDEO (next frame)  -S-   _

Relaxed VGA Timing
<ul> <li>This all sounds pretty strict and exact</li> <li>It's not really The only things a VGA monitor really cares about are:</li> <li>Hsync</li> <li>Vsync</li> <li>Actually, all it cares about is the falling edge of those nulsed.</li> </ul>
<ul> <li>The beam will retrace whenever you tell it to</li> <li>It's up to you to make sure that the video signal is 0v when you are not painting (i.e. retracing)</li> </ul>

















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The Character ROM bit binary address A[ of the selected chara	contains the 64 m 4:0] and a 16-bit u cter at a time on t	nember ASCII upp Inary decoded ad he signals T[7:0].	er-case chara dress, nOE0-	acter set nOE120	. The charact . The Charact	ers are addressed with a 5 ter ROM outputs a single re
AI4:31 decodes one o	f the four rows of	16 characters in	he ROM			
A[4:3] == 0	- first row	" 1"#S%&! ()	*+. = . /"			
A[4:3] == 1	- second row	*0123456789	::<=>?"			
A[4:3] == 2	- third row	"RABCDEFGHI	JKLMNO"			
A[4:3] == 3	- fourth row	"PQRSTUVWXY	Z[\]^ "			
The sixteen signals n nOE96, nOE104, nO and only one is asser it and nOE7==0 sele	OE0, nOE8, nOE E112, nOE120 se ted at any time. I cts "' 7GW".	16, nOE24, nOE3 lect one of the size For instance, nOE	32, nOE40, no teen columns 0==0 selects	DE48, n s of of fo the first	DE56, nOE64, ur characters. column with th	nOE72, nOE80, nOE88, These signals are active I he four characters " 00P"
The sixteen signals n nOE96, nOE104, nO and only one is asser- it and nOE7==0 sele [2:0] decodes one of nen A[2:0] will produce	OE0, nOE8, nOE E112, nOE120 set ted at any time. I cts "'7GW", the eight charact e the following bi	16, nOE24, nOE3 lect one of the size For instance, nOE er rows. For inst nary output on T	82, nOE40, nO teen columns 0==0 selects ance, if the cl 7:0].	DE48, no s of of fo the first naracter	DE56, nOE64, ur characters. column with th "A" is selecte	nOE72, nOE80, nOE88, These signals are active I he four characters " 00 ₽″ d with A[4:3]==2 and nOE
The sixteen signals n nOE96, nOE104, nO and only one is asser it and nOE7==0 sele [2:0] decodes one of nen A[2:0] will product	OE0, nOE8, nOE E112, nOE120 set ted at any time. I cts "'7GW". the eight charact e the following bi	16, nOE24, nOE3 lect one of the six For instance, nOE er rows. For inst nary output on T Binary	32, nOE40, nG teen columns 0==0 selects ance, if the cl 7:0]. Visible O	DE48, n0 s of of fo the first naracter utput	DE56, nOE64, ur characters. column with ti "A" is selecte	nOE72, nOE80, nOE88, These signals are active I he four characters " 08 P" d with A[4:3]==2 and nOE
The sixteen signals n nOE96, nOE104, nO and only one is asser it and nOE7==0 sele [2:0] decodes one of nen A[2:0] will produce A[2:0] == 0	OE0, nOE8, nOE E112, nOE120 se ted at any time. I cts "'7GW". the eight charact e the following bi - first row	16, nOE24, nOE3 lect one of the sis For instance, nOE er rows. For inst nary output on T Binary 00011100	32, nOE40, nO teen columns 0==0 selects ance, if the cl 7:0]. Visible O	DE48, nG s of of fo the first naracter utput	DE56, nOE64, ur characters. column with ti "A" is selecte	nOE72, nOE80, nOE88, These signals are active in he four characters " 08P" d with A[4:3]==2 and nOE
The sixteen signals n nOE96, nOE104, nO and only one is asser it and nOE7==0 sele [2:0] decodes one of nen A[2:0] will produc A[2:0] == 0 A[2:0] == 1	OE0, nOE8, nOE E112, nOE120 se ted at any time. I cts "'7GW". the eight charact e the following bi - first row - second row	16, nOE24, nOE3 lect one of the sis For instance, nOE ter rows. For inst nary output on T Binary 00011100 0010010	32, nOE40, nO teen columns (0==0 selects ance, if the cl 7:0]. Visible O ***	DE48, nG s of of fo the first naracter utput	DE56, nOE64, ur characters. column with tl "A" is selecte	nOE72, nOE80, nOE88, These signals are active I he four characters " 08°F" d with A[4:3]==2 and nOE
The sixteen signals n nOE96, nOE104, nO and only one is asset it and nO27=0 sele [2:0] decodes one of hen A[2:0] will produc A[2:0] == 0 A[2:0] == 1 A[2:0] == 1	OE0, nOE8, nOE E112, nOE120 se ted at any time. I cts "'7GW". the eight charact e the following bi - first row - second row - third row	16, nOE24, nOE3 lect one of the sis For instance, nOE er rows. For inst nary output on T Binary 00011100 00100010 00100010	32, nOE40, nO teen columns 0==0 selects ance, if the cl 7:0]. Visible O	DE48, nG s of of fo the first naracter utput	DE56, nOE64, ur characters. column with tl "A" is selecte	nOE72, nOE80, nOE88, These signals are active I he four characters <sup>™</sup> 082" d with A[4:3]==2 and nOE
The sixteen signals n nDE96, nDE104, nD and only one is asset it and nD27=0 sele [2:0] decodes one of nen A[2:0] will produce A[2:0] == 0 A[2:0] == 2 A[2:0] == 2 A[2:0] == 2	OE0, nOE8, nOE E112, nOE120 se ted at any time. I cts "'76W". the eight charact e the following bi - first row - second row - firdt row - fourth row	16, nOE24, nOE3 lead one of the six For instance, nOE er rows. For inst nary output on T Binary 00011100 00100010 00100010 00111110	32, nOE40, nC teen columns 0==0 selects ance, if the cl 7:0]. Visible O	DE48, nC s of of fo the first naracter utput * *	DE56, nOE64, ur characters. column with tl "A" is selecte	nOE72, nOE80, nOE88, These signals are active the he four characters " 08 P" d with A[4:3]==2 and nOE
The sixteen signals n nDE96, nDE104, nD and only one is asset it and nO27=0 sele [2:0] decodes one of ten A[2:0] will produce A[2:0] == 0 A[2:0] == 1 A[2:0] == 2 A[2:0] == 2 A[2:0] == 4	OE0, nOE8, nOE E112, nOE120 se ted at any time. I cts "'7GW". the eight charact e the following bi - first row - second row - third row - fourth row - fifth row	16, nOE24, nOE3 elect one of the sis For instance, nOE er rows. For inst nary output on T Binary 00011100 00100010 0010010 00111110 001001	32, nOE40, nC teen columns 0==0 selects ance, if the ch 7:0]. Visible O ***	DE48, nG s of of fo the first naracter utput * * * *	DE56, nOE64, ur characters. column with tl "A" is selecte	nOE72, nOE80, nOE88, These signals are active to four characters " O8F" d with A[4:3]==2 and nOE
The sixteen signals n nDE96, nDE104, nD and only one is asset it and nDY one is asset it and nDE7==0 sele [2:0] decodes one of nen A[2:0] will produc A[2:0] == 0 A[2:0] == 2 A[2:0] == 3 A[2:0] == 3 A[2:0] == 3 A[2:0] == 5 A[2:0] == 5 A[2:0] == 3 A[2:0] == 5 A[2:0] == 5 A[2	OE0, nOE8, nOE E112, nOE120 se ted at any time, i cts ** 7Gw**. the eight charact a the following bi - first row - second row - fund row - fund row - fund row - sixth row	16, nOE24, nOE: For instance, nOE er rows. For instance, nOE er rows. For instance, nOE Binary 00011100 00100010 00100010 00111110 00100010	32, nOE40, nC teen columns 0==0 selects ance, if the cl 7:0]. Visible O ***	DE48, nG s of of fo the first naracter utput * * * * *	DE56, nOE64, ur characters. column with tl "A" is selecte	nOET2, nOE80, nOE88, These signals are active he four characters " 08P" d with A[4:3]==2 and nOE
The sixteen signals n nOE96, nOE104, nO and only one is asset it and nOE7==0 sele [2:0] decodes one of hen A[2:0] will produc A[2:0] == 1 A[2:0] == 1 A[2:0] == 1 A[2:0] == 3 A[2:0] == 4 A[2:0] == 4 A[2:0] == 4 A[2:0] == 4	OE0, nOE8, nOE E112, nOE120 set dat any time. I cts " 7GW", the eight charact e the following bi - first row - second row - third row - fourth row - firth row - sixeth row - seventh row	16, nOE24, nOE3 lect one of the sile for instance, nOE er rows. For instance, nOE 00011100 0010010 0010010 0010010 001001	32, nOE40, nC teen columns 0==0 selects ance, if the cl 7:0]. Visible O ****	DE48, nG s of of fo the first naracter utput * * * * *	DE56, nOE64, ur characters. column with tl "A" is selecte	nOET2, nOE80, nOE88, These signals are active he four characters " 08 p <sup>er</sup> d with A[4:3]==2 and nOE



















## makemem Limits Number of rows is limited to 64 by address decoder design Columns are not restricted For ROM you can add a tristate bus at the output which ia another level of decoding width must be an even number SRAM has single, dual, and triple port options

