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LPM Quick Reference Guide

Data Sheets

Altera Device Package Information Data Sheet

Altera Programming Hardware Data Sheet

BitBlaster Serial Download Cable Data Sheet

ByteBlaster Parallel Port Download Cable Data Sheet

ByteBlasterMV Parallel Port Download Cable Data Sheet

Configuration Elements Data Sheet

Configuration Devices for APEX & FLEX Devices Data Sheet

FLEX 10K Embedded Programmable Logic Family Data Sheet

FLEX 10KE Embedded Programmable Logic Family Data Sheet

FLEX 10K PCI Prototype Board Data Sheet

FLEX 10KE PCI Development Board Data Sheet

MasterBlaster Serial/USB Communications Data Sheet

Operating Requirements for Altera Devices Data Sheet

QFP Carrier & Development Socket Data Sheet

General Information

Introduction (to the Altera *1999 Data Book*)

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Product Information Bulletins

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- PIB 21 Implementing Logic with the Embedded Array in FLEX 10K Devices
- PIB 22 Design Tools for 100,000 Gate Programmable Logic Devices
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Selector Guides

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Technical Briefs

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Note:

(1) Although this document was originally written for FLEX 8000 designs, it can also be used for FLEX 10K designs.