



November 2000

## Application Briefs

- AB 124 Prescaled Counters in FLEX 8000 Devices
- AB 130 Parity Generators in FLEX 8000 Devices
- AB 131 State Machine Encoding
- AB 135 Ripple-Carry Gray Code Counters in FLEX 8000 Devices

## Application Notes

- AN 33 Configuring FLEX 8000 Devices
- AN 36 Designing with FLEX 8000 Devices
- AN 38 Configuring Multiple FLEX 8000 Devices
- AN 39 IEEE 1149.1 (JTAG) Boundary-Scan Testing in Altera Devices
- AN 41 PCI Bus Applications in Altera Devices
- AN 42 Metastability in Altera Devices
- AN 46 ATM Packet Scheduler in FLEX 8000 Devices
- AN 49 Implementing CRCCs in Altera Devices
- AN 51 Using Programmable Logic for Gate Array Designs
- AN 71 Guidelines for Handling J-Lead & QFP Devices
- AN 73 Implementing FIR Filters in FLEX Devices
- AN 74 Evaluating Power for Altera Devices
- AN 75 High-Speed Board Designs
- AN 76 Understanding FLEX 8000 Timing
- AN 80 Selecting Sockets for Altera Devices
- AN 81 Reflow Soldering Guidelines for Surface-Mount Devices
- AN 82 Highly Optimized 2-D Convolvers in FLEX Devices
- AN 83 Binary Numbering Systems
- AN 107 Using Altera Devices in Multiple Voltage Systems

## **Catalogs**

Intellectual Property Catalog

LPM Quick Reference Guide

## **Customer Applications**

FLEX 8000 Devices “Grab” Vitana’s Fancy Customer Application

## **Data Sheets**

Altera Device Package Information Data Sheet

Altera Programming Hardware Data Sheet

BitBlaster Serial Download Cable Data Sheet

ByteBlaster Parallel Port Download Cable Data Sheet

ByteBlasterMV Parallel Port Download Cable Data Sheet

Configuration Elements Data Sheet

Configuration Devices for APEX & FLEX Devices Data Sheet

FLEX 8000 Programmable Logic Device Family Data Sheet

MasterBlaster Serial/USB Communications Data Sheet

Operating Requirements for Altera Devices Data Sheet

## **General Information**

Introduction (to the Altera *1999 Data Book*)

Ordering Information

## **Product Information Bulletins**

PIB 23 Digital Signal Processing in FLEX Devices

## **Selector Guides**

Component Selector Guide

## **Technical Briefs**

TB 3 FLEX Devices as Alternatives to ASSPs & ASICs

TB 4 Using FLEX Devices as DSP Coprocessors

TB 8 Implementing Multirate Filters in FLEX Devices

TB 24 The Advantages of LPM