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New Documents

AN 129 Implementing a W-CDMA System with Altera Devices & IP Functions
Component Selector Guide
Configuring PLDs with FLASH Memory White Paper
Designing Wireless Base Stations with APEX CAM White Paper
MAX+PLUS II Read Me File
Hadamard Transform Processor White Paper
Quartus Installation & Licensing for PCs ver. 2000.09
Quartus Installation & Licensing for UNIX Workstations ver. 2000.09
SB 48 Reed-Solomon Compiler MegaCore Function
SB 49 NCO Compiler MegaCore Function
SB 50 Turbo Encoder/Decoder MegaCore Function
TB 70 Jitter Comparison Analysis: APEX 20KE PLL vs. Virtex-E DLL

Updated Documents

AN 39 IEEE 1149.1 (JTAG) Boundary-Scan Testing in Altera Devices
AN 90 SameFrame Pin-Out Design for FineLine BGA Packages
AN 115 Using the ClockLock & ClockBoost PLL Features in APEX Devices
AN 128 Implementing Voice Over Internet Protocol
ARM-Based Embedded Processor Device Overview Data Sheet
FLEX 10KE Embedded Programmable Logic Family Data Sheet
Installing the Visual IP Software User Guide
MAX 7000B Programmable Logic Device Family Data Sheet
MAX 3000A Programmable Logic Device Family Data Sheet
MIPS-Based Embedded Processor Device Overview Data Sheet
PIB 21 Implementing Logic with the Embedded Array in FLEX 10K Devices
SB 42 Interleaver/Deinterleaver MegaCore Function