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Overview

The E+MAX software is a complete, free software package that allows you to design for and program the industry's most popular product-termbased programmable logic devices (PLDs): the Altera® MAX® 7000, MAX 7000S, MAX 7000A, MAX 7000AE, MAX 7000B, and MAX 3000A devices. The E+MAX software is based on the industry-standard MAX+PLUS® II software and includes both VHDL and Verilog HDL design entry and synthesis. Table 1 lists the E+MAX features.

Table 1. E+MAX Software Features	
Features	Description
Device Support	 MAX 7000 device family (including MAX 7000A, MAX 7000AE, MAX 7000B, MAX 7000E, and MAX 7000S devices) MAX 3000A device family
Design Entry	 Text-based design entry using VHDL, Verilog HDL, or the Altera Hardware Description Language (AHDL) Schematic design entry Interfaces to popular EDA tools Floorplan editing Hierarchical design management Library of parameterized modules (LPM)
Design Compilation	 Logic synthesis and automatic fitting Automatic error location in design files
Design Verification	 Timing analysis Functional simulation Timing simulation Creates output files for use with third-party simulators
Programming	Device programming (1)
Other features	 OpenCoreTM evaluation for Altera MegaCoreTM functions and Altera Megafunction Partners Program (AMPPSM) functions On-line help

Note:

(1) You can use the Altera Stand-Alone Programmer (ASAP2) software, which is a stand-alone version of the MAX+PLUS II Programmer application, to program, verify, examine, and test Altera devices without having the full version of the MAX+PLUS II software. You can download ASAP2 from the Altera web site at http://www.altera.com.

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System Requirements

The E+MAX software runs on Windows 95/98 or Windows NT 4.0 on stand-alone PCs or PCs in a TCP/IP networked environment. Refer to the MAX+PLUS II **read.me** file for additional system requirements.



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