



Angelo Family Errata Sheet

Introduction

This document describes the errata information on the Angelo family devices. This errata sheet should be compared with the Angelo family datasheet version 1.1 to fully understand the device functions.

Errata Summary

The following table summarizes the known issues and which device revision each issue affects. See Errata Details for a detailed description of each issue.

Issue	Affected Device
PLL input reference range	AG1F4N4T100GC7
	AG1F4N4L144GC7
	AG1F4N4Q208GC7
	AG1F4Q208GC7
Invalid user I/Os	AG1F4N4L144GC7

Errata Details

This section provides the detailed errata information on each issue.

1. PLL input reference range

As recently verified, the PLL input reference range should be 10-350MHz.

2. Invalid user I/Os

Four user I/Os of the AG1F4N4L144GC7 device, I/O 33, 34, 83 and 84, are invalid. The corresponding pin numbers of these four I/Os are pin 61, 62, 125 and 126.

About Agate Logic

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