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0541	AT28C17	11/98	12
0006	AT28C256	12/99	18
0001	AT28C64/X	12/99	12
0270	AT28C64B	12/99	13
0007	AT28HC256	12/99	16
0274	AT28HC64B	12/99	14
0395	AT28LV010	11/98	12
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0180	AT24C01A/02/04/08/16	12/98	22
0976	AT24C02A/04A/08A	11/98	16
0670	AT24C128/256	10/99	16
0105	AT24C164	12/98	12
0405	AT24C21	1/00	14
0336	AT24C32/64	12/98	16
1116	AT24C512 Preliminary	10/99	12
1152	AT24CS128/256	11/98	16
0606	AT25010/020/040	11/98	16
0675	AT25080/160/320/640	9/99	17
0872	AT25128/256	9/99	18
1082	AT25P1024 Preliminary	10/99	14
0958	AT34C02	1/00	13
0173	AT59C11/22/13	12/98	14
0172	AT93C46/56/57/66	10/99	16
0539	AT93C46A	11/98	12
1122	AT93C46C	11/98	12
1237	AT93C86 Advance Information	1/99	11
1113	AT25HP256/512 Preliminary	1/99	17

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0507	Interfacing 24CXX Serial EEPROMs with AT89CX051 MCU	11/98	4
0521	Interfacing 93CXX Serial EEPROMs with AT89CX051 MCU	11/98	4
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1083	AT40K-PCI Core	2/99	26
1132	AT40K-FFT	12/98	8

FPGA Development Tools

1421	Atmel FPGA Integrated Development System	3/00	8
0452	AT6000 FPGA Integrated Development System Prototype Kit	3/00	7
1423	ATSTK40 FPGA Starter Kit	3/00	2
0373	Component Generators Handbook (summary)	5/99	9

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0436	AT6000 Series Configuration	9/99	21
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0460	Recommended Design Methods	9/99	9
0461	Implementing Cache Logic with FPGAs	9/99	5
0462	Data Acquisition Systems Using Cache Logic FPGAs	9/99	5
0463	High-Speed, Loadable 16 Bit Binary Counter	9/99	5
0464	Compact, Loadable 16 and 32 Bit Binary Counters	3/97	3
0465	16 Bit Up/Down Counter Shift Register	9/99	4
0466	9 Bit Programmable Terminal Counter	9/99	5
0467	16 Bit Carry-Select Adder	9/99	3
0468	Ripple-Carry Adders	9/99	3
0469	Barrel Shifter	9/99	3
0470	24 Bit Magnitude Comparator with 50 ns Response	9/99	5
0471	16 Bit, Four-To-One Multiplexer with 15ns Delay	9/99	3
0472	8 Bit S-P/P-S Corner-Bender Data Converter	9/99	5
0473	16 Word by 8 Bit FIFO	9/99	5
0474	IEEE 11491-1990 Standard Test Access Port & Boundary-Scan	9/99	8

0475	Digital Frequency/Phase Comparator (DFPC)	9/99	4
0476	Configuration Compression Algorithm	9/99	2
0477	Modeling Device Power Consumption	9/99	3
0771	Edge Detection in AT6000 FPGAs	3/97	5
0764	3 x 3 Convolver with Run-time Reconfigurable Vector Multiplier in Atmel AT6000 FPGAs	8/99	9
0724	DSP Acceleration Using Reconfigurable Coprocessor FPGA	9/99	6
0716	Implementing Bit-Serial Digital Filters in AT6000 FPGAs	4/97	12
0831	Standard 8-tap FIR Filter Macro (FIR8)	9/97	3
0832	Symmetrical 8-tap FIR Filter Macro (FIR8S)	9/97	3
0833	Symmetrical 16-tap FIR Filter Macro (FIR16S)	9/97	2
0834	Symmetrical 24-tap FIR Filter Macro (FIR24S)	9/97	2
0835	Symmetrical 32-tap FIR Filter Macro (FIR32S)	9/97	2
0836	Second-Order IIR Digital Filter Macro (IIR)	9/97	3
0529	FPGA-based FIR Filter Using Bit-Serial Digital Signal Processing	9/99	10
1449	Replacement of a RAM w/Atmel FreeRAM in VHDL	2/00	8

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0996	AT17C/LV65A/128A/256A	10/99	14
1239	AT17C/LV020	5/99	12
0391	AT17C65/128/256/LV65/128/256	7/99	13
0974	AT17C/LV512A/C/LV010A	7/99	13
0944	AT17LV512/010/C512/010	7/99	12
0642	ATDH2200 Configurator Programming Kit (Enhanced)	7/99	3
1444	HDL Planner: Design Development Environment for HDL-based FPGA Designs	10/99	8

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0437	Programming Specification for Atmel's AT17 and AT17A Series FPGA Configuration EEPROMs	7/99	28
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0910	AT17A Series Conversions from Altera FPGA Serial Configuration Memories	10/99	13
1450	AVR301: C Code for Interfacing AVR to AT17CXXX FPGA Configuration Memories	10/99	18

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Programmable Logic Device (SPLD's & CPLD's) Datasheet

SPLD – Industry Standard

0413	Atmel's Broad EPLD Product Line-Introduction	7/99	4
0364	ATF16V8B (Q) (L)	8/99	19
0425	ATF16V8C	8/99	18
0453	ATF16V8CZ	8/99	12
0404	ATF16LV8CZ Advanced Information	12/99	3
0403	ATF16LV8C	7/99	11
0407	ATF20V8B/BQ/BQL	6/99	18
0408	ATF20V8C(Q)	3/00	3
0454	ATF20V8CZ(Q)	3/00	3
0250	ATF22V10B(Q)(QL)	3/00	15
0735	ATF22V10C(Q)(QZ)	3/00	15
0778	ATF22V10CZ	8/99	13
0190	AT22LV10(L)	8/99	12
0780	ATF22LV10C	7/99	14
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CPLD – 2 22V10s in 24-Pin Package

0024	ATV750(L)	3/00	15
0301	ATV750B(L)	3/00	17
0776	ATF750C(L)	1/00	16
1445	Using Active-VHDL Design Entry and Behavioral Simulation with Atmel IDS 6.0	2/00	14
1447	ATF750LVC	2/00	14

CPLD – Industry Compatible

0759	ATF1500A(L)	6/99	17
0723	ATF1500ABV(L)	12/99	13
1615	ATF1502ASV/ATF1502ASVL Preliminary	2/00	18

0950	ATF1504AS(L)	1/00	25
1409	ATF1504ASV(L)	1/00	21
0784	ATF1508AS(L)	1/00	26
1408	ATF1508ASV(L)	1/00	22
0995	ATF1502AS(L)	1/00	20
0994	ATF1516AS(L) Preliminary	9/99	13
CPLD – Proprietary			
0025	ATV2500H/L	6/98	15
0249	ATV2500B(Q)(L)	3/00	20
0777	ATFV2500C Advanced Information	12/99	3
Software Datasheets			
0714	ATDS1100PC/1120PC/1130PC/1140PC	9/99	8
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0239	Atmel PLDs' Architectures Simplify Timing Calculations	7/99	6
0251	ATV2500 Application Example: Video Frame Grabber	7/99	11
0718	OrCAD Support for Atmel PLDs	9/99	5
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0479	Tips on Using Test Vectors for Atmel PLDs	9/99	20
0609	Using the ATF1500(A) CPLD	9/99	16
0731	ATF1500 44-Pin Complex PLD	9/99	5
0924	Designing for In-System Programmability with Atmel CPLDs	9/97	8
0916	ATF1500AS Product Family Conversion	4/99	7
0929	ATF1500AS Family ISP Board	12/98	20
1651	In-System Programming of Atmel ATF1500AS Devices on the HP3070	4/00	10
0790	Atmel PLD Frequently Asked Questions-Design Guidelines	7/99	11
0478	Introduction to the SMD Product Listing	3/00	12
1613	Creating Atmel JAM/JBC File(s) for the ATF1500AS Device Family	12/99	4
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0429	PLD Software Tools Overview	8/99	3
0433	New Programmer Support Information	8/99	2
0434	ATDS1000PC	8/99	2
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1138	AT94K FPSLIC (Summary)	12/99	4
1482	Field Programmable System Level Integrated Circuit	10/99	2
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0552	Manufacturing & Test Overview of Military Products	11/98	8
0538	Explanation of Atmel's Part Number Code	12/98	1
1238	Corporate Quality Policy	2/99	1
0031	Product Guide – Sept 99	9/99	20
0409	Atmel North American Rep. Listings	2/00	3
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1095	8C, 8-lead, 0.230" Wide, LAP	6/99	1
1096	8CI, 8-lead, 0.300" Wide, LAP	6/99	1
1091	14S, 14-Lead, 0.150" Wide, Plastic Gull Wing Small Outline (JEDEC SOIC)	5/98	1

1092	14T, 14-Lead, 0.170" Wide, Thin Shrink Small Outline Package (TSSOP)	5/98	1
1093	16S1, 16-Lead, 0.150" Wide, Plastic Gull Wing Small Outline (JEDEC SOIC)	5/98	1
1094	20T, 20-Lead, 0.170" Wide Thin Shrink Small Outline Package (TSSOP)	5/98	1
1097	8M, 8-Lead, 0.118" Wide, Miniature Small Outline Package (MSOP)	5/98	1
1098	8P3, 8-Lead, 0.300" Wide, Plastic Dual Inline Package (PDIP)	5/98	1
1099	8S1, 8-Lead, 0.150" Wide, Plastic Gull Wing Small Outline (JEDEC SOIC)	5/98	1
1101	8S2, 8-Lead, 0.210" Wide, Plastic Gull Wing Small Outline (EIAJ SOIC)	5/98	1
1101	8T, 8-Lead, 0.170" Wide, Thin Shrink Small Outline Package (TSSOP)	5/98	1
1256	16S2, 16-Lead, 0.300” Wide, JEDEC SOIC	12/98	1
1115	20S,20-Lead, 0.300” Wide, EIAJ SOIC	12/98	1
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