



05/14/99

Errata: CS61600 Rev. G - PCM Jitter Attenuator

(Reference CS61600 Data Sheet DS9F3 dated APR '90)

The current devices do not pass the 1500V Electro Static Discharge ESD (Human Body Model) requirement for high volume production under Cirrus Logic QPL III quality and manufacturability criteria. Typical devices pass at 1.0kV and fail at 1.1kV. The devices pass the Machine Model test at 200V and latchup at 400mA.

If there are any questions concerning this information,
Please contact: Clay Olmstead @ (512) 912-3953
