



REQUESTING A CL8000 FIRST ARTICLE

Introduction

This document will provide you with the information necessary for submitting a design file to Clear Logic. It will outline the data required to generate first articles, as well as the procedure for providing this data to Clear Logic. It will describe in general terms how your prototypes will be processed, and will provide guidelines for approving the prototypes once you receive them.

Data Required

In order to produce prototypes for a given FLEX 8000 design, Clear Logic needs [1] a completed First Article Request Form, and [2] a copy of the bitstream output file (.HEX, .POF, or .TTF filename extension) from the Max+Plus® II design software. A copy of the First Article Request Form is included in this document.

The First Article Request Form contains contact information as well as some basic information regarding the configuration scheme used by the target system. This information must be completed to ensure that the first articles are processed correctly.

Submitting a Design

The first article request can be submitted either electronically or in hard copy form. To request a prototype electronically, point a Netscape 2.0 compatible browser to www.clear-logic.com/firstarticle. You will be directed to a web-enabled version of the First Article Request Form, which will collect the necessary information from you, and which will allow you to attach the bitstream file. Inquiries regarding first article requests can be emailed to firstarticle@clear-logic.com.

Email is another popular method for submitting first article requests. Clear Logic provides an electronic Excel worksheet version of the First Article Request Form for you to download at www.clear-logic.com/firstarticle. Just attach the completed form and the bitstream file to your email message. Send your email to firstarticle@clear-logic.com.

First Article Request		"green" cells MUST be filled out; "Blue" (dark) cells as appropriate or available
Sales Representative:	<input style="background-color: #c8e6c9;" type="text"/>	Date: <input style="background-color: #bbdefb;" type="text"/>
Rep Company:	<input style="background-color: #c8e6c9;" type="text"/>	Distributor Salesman: <input style="background-color: #bbdefb;" type="text"/>
		Distributor Company: <input style="background-color: #bbdefb;" type="text"/>
Customer Information		
Customer:	<input style="background-color: #c8e6c9;" type="text"/>	New Design? (1=Yes, 2=No): <input style="background-color: #c8e6c9;" type="text"/>
Account No.:	<input style="background-color: #bbdefb;" type="text"/>	Revision of following device: <input style="background-color: #bbdefb;" type="text"/>
End Customer:	<input style="background-color: #c8e6c9;" type="text"/>	
Purchasing Contact:	<input style="background-color: #c8e6c9;" type="text"/>	phone: <input style="background-color: #c8e6c9;" type="text"/>
Technical Contact:	<input style="background-color: #bbdefb;" type="text"/>	phone: <input style="background-color: #bbdefb;" type="text"/>
Bitstream File Information: Acceptable Formats Include .HEX, .POF, and .TTF		
File Name:	<input style="background-color: #c8e6c9;" type="text"/>	
Revision Date:	<input style="background-color: #bbdefb;" type="text"/>	
Design Information		
Complete Part No.:	<input style="background-color: #c8e6c9;" type="text"/> (incl. package and speed grade)	
No. of First Articles:	<input style="background-color: #c8e6c9;" type="text"/>	NOTE: Sales Mgr. approval required for > 2 first articles.
1. Please indicate the configuration scheme used by the target system: <input style="background-color: #c8e6c9;" type="text"/>		
AS = Active Serial APU = Active Parallel Up PPS = Passive Parallel Synchronous		
PS = Passive Serial APD = Active Parallel Down PPA = Passive Parallel Asynchronous		
2. "Instant On" bypasses the configuration sequence and eliminates the need for an external EPROM.		
Is this option desired? (1=Yes, 2=No) <input style="background-color: #c8e6c9;" type="text"/> If YES, a Technical Contact MUST be provided above.		
3. Clear Logic components are NOT suitable for applications employing in-system reconfigurability.		
Does the target system use in-system reconfigurability? (1=Yes, 2=No) <input style="background-color: #c8e6c9;" type="text"/>		
Shipping Information		
Ship to the following address. If left blank, first articles will be sent to the sales representative listed above.		
Name:	<input style="background-color: #c8e6c9;" type="text"/>	
Address:	<input style="background-color: #c8e6c9;" type="text"/>	
City:	<input style="background-color: #c8e6c9;" type="text"/>	
State:	<input style="background-color: #c8e6c9;" type="text"/>	
Country:	<input style="background-color: #c8e6c9;" type="text"/>	
Phone Number:	<input style="background-color: #c8e6c9;" type="text"/>	

Figure 1. First Article Request Form (Revised August, 1998)



You may also request a first article using a paper hard copy of the First Article Request Form, submitted with your bitstream on a floppy disk. We recommend that all hard copy/floppy first article requests be submitted by way of your local Clear Logic sales representative or distributor. To submit a hard copy directly to Clear Logic, please send the completed form and the floppy via Fedex to *First Articles, Product Marketing Group, Clear Logic, Inc.; 2972 Stender Way; Santa Clara, CA 95054*; the telephone number is (888) 548-7788 (toll free within the United States) or (408) 492-8585.

First Article Processing

Once your first article request is received, Clear Logic processes it to create your prototypes:


Verification: All forms are verified to be completely filled out. You may be contacted by your Clear Logic sales person to verify information regarding your requirements. The design file will be checked to ensure that it is a valid file for the type of device you have requested.

Part Number Creation: Once the first article package is complete and valid, a part number for the device is created. This part number (CLD prefix) will be used in all subsequent transactions to identify the device type, bitstream file, packaging, marking information, and any special processing. An order will be entered for the first articles. Clear Logic will provide two units for you to evaluate.

Design Package Archive: All design files and forms will be archived at Clear Logic, using a write-once-read-many (WORM) CD drive, for future reference.

Release: The design package is released for first article processing. A specification (Figure 2) for the device is created and logged into document control.





CLD2013A

Device Specification

Description
CL8452ALC84-3, 84-pin PLCC version.

Attachments
CL8452AL datasheet, Package Diagram

Supplemental Specifications

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	Comment

Reference Table

Parameter	Value
Target FPGA	EPF8452ALC84-3
Bitstream File Name	ASJ8452.HEX
Bitstream File Size	18.1 KB
End Customer:	Company XYZ
Package Type	PLCC-84
Speed Grade	-3
Configuration Mode	Instant On

NOTE: First Articles are suitable for functional evaluation over the operating range. They are not suitable for stress testing, such as burn-in or temperature cycling.

Special Processing

Device Marking

First Line: STANDARD Custom Logo?

Second Line: NONE Yes No

NOTE: Each part will also be marked with a 5-digit date code, which denotes the year, work week, and day of manufacture

Document No.
PDN-0010

Revision Date
8/28/98

Most Recent Change
Created

Figure 2. Device Specification Example

Processing: The first articles are laser-configured and assembled into special epoxy-filled fast-turn packaging. These packages have electrical characteristics identical to Clear Logic’s injection molded production packages. The fast-turn packages provide a convenient vehicle for you to quickly verify the functioning of Clear Logic devices in your system. Clear Logic also creates and archives test programs and other device-associated files.

Shipment: The first articles are shipped to you, along with the appropriate device specification and evaluation form.



Notices

This document revised 28 August, 1998

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