



Quick Reference Product Guide

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General

pASIC DEVELOPMENT TOOLS

Part #	Product Name	Description
PC		
QT-QWK-51-PC-A ^{1,2}	QuickWorks Toolkit	Complete design kit for Windows-PC with Programmer
QT-QTL-51-PC-A ^{1,2}	QuickTools Toolkit	Includes QS-SPDE-51-PC, QS-UTIL-51-PC and programmer
QS-QWK-51-PC ²	QuickWorks Software	Complete software development for Windows-PC
QS-QTL-51-PC ²	QuickTools Software	Includes QS-SPDE-51-PC and QS-UTIL-51-PC
QT-DP-51-PC-A ^{1,2}	Designer Programmer	Up to 8 programmers may be ganged off one computer
QT-DFP-60-PC-A ^{1,2}	DeskFab	Allows simultaneous programming of up to 32 pASIC1 or pASIC2 devices
QS-QWK-51-PC-EV ²	Evaluation Software	Complete a full design! Includes 30 day license & support
QS-VL-20-PC ²	Viewlogic Libraries	For Workview Pro, Plus; supports Draw, Sim, Synthesis
QS-VBST-10-PC ²	Veribest Libraries	For support of Veribest (Intergraph) design tools
QS-SPDE-51-PC ^{2,3}	SpDE Software	Place / route / timing analysis for 3rd party tools
QS-UTIL-51-PC ^{2,3}	Software Utilities Pkg	3rd Party simulation interfaces, logic re-optimizer
SUN		
QS-QTL-51-SUN ²	QuickTools Software	Includes QS-SPDE-51-SUN & QS-UTIL-51-SUN
QS-VL-20-SUN ²	Viewlogic Libraries	Support for Powerview, including ViewSynthesis
QS-SYN-10-SUN ²	Synopsys Libraries	Support for Design Compiler with VHDL & Verilog
QS-MEN-10-SUN ²	Mentor Libraries	Support for Mentor schematic capture and simulator
QS-CNC-10-SUN ²	Cadence Libraries	Support for Concept schematic package
QS-SPDE-51-SUN ^{2,3}	SpDE Software	Place / route / timing analysis for SUN SparcStation
QS-UTIL-51-SUN ^{2,3}	Software Utilities	3rd Party simulation interfaces, logic re-optimizer
HP		
QS-QTL-51-HP ²	QuickTools Software	Includes QS-SPDE-51-HP & QS-UTIL-51-HP
QS-MEN-10-HP ²	Mentor Libraries	Support for Mentor schematic capture and simulator
QS-SPDE-51-HP ^{2,3}	SpDE Software	Place / route / timing analysis for HP Workstation
QS-UTIL-51-HP ^{2,3}	Software Utilities	3rd Party simulation interfaces, logic re-optimizer

Notes:

- (1) QT part numbers include programmer power designators: "-A" for 110V, "-B" for 220V, or "-C" for 240V (UK).
- (2) Software version number in part number subject to change (ex: "-51" will change to "60").
- (3) QS-QTL is the most cost-effective bundle of QS-SPDE plus QS-UTIL.



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Part #	Usable Gates	Logic Cells	Max Flip - Flops	I/O Pins
pASIC 1 Family				
QL8x12	1,000	96	96	64
QL12x16	2,000	192	192	88
QL16x24	4,000	384	384	122
QL24x32	8,000	768	768	180
pASIC 2 Family				
QL2003	3 - 4,000	192	408	120
QL2005	5 - 6,500	320	636	156
QL2007	7 - 9,000	480	912	192
QL2009	9-11,500	672	1236	228
QL2012	12-15,500	896	1608	264
QL2016	16-20,500	1152	2028	300
QL2020	20-25,500	1440	2496	336

Part #	PLCC			TQFP		VQFP	PQFP	CPGA		CQFP ¹			PBGA		
	44	68	84	100	144	100	208	84	144	100	160 ²	208	256	352	>500
pASIC 1 Family															
QL8x12	•	•		•		(•)									
QL12x16		•	•	•		•		•		•					
QL16x24			•	•		(•)			•		•				
QL24x32					•			•				•			
pASIC 2 Family															
QL2003			•		•	(•)									
QL2005			•		•	(•)	•						(•)		
QL2007			•		•		•						(•)		
QL2009					•		•						(•)		
QL2012							•							(•)	
QL2016							•							(•)	(•)
QL2020							•							(•)	(•)

Notes:

(•) = Package Availability to be Determined

(1) CQFP ship standard in molded plastic carrier ring to maintain lead integrity

(2) CQFP Excised from carrier ring with leads trimmed and formed available