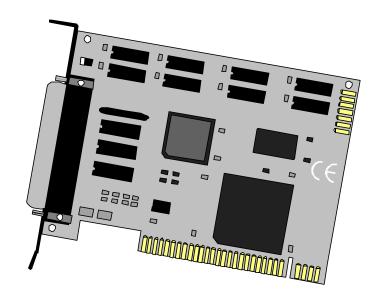


DIO-16.PCI TM USER MANUAL



Part # 8002

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Introduction

Overview

The **DIO-16.PCI** provides 8 reed relays that can latch power, data or other electronic signals for control applications and 8 optically isolated inputs to allow monitoring of off board switch closures, relays or for any other general purpose monitoring needs. The **DIO-16** is PC compatible and PCI slot.

What's Included

The **DIO-16.PCI** is shipped with the following items. If any of these items is missing or damaged, contact the supplier.

- DIO-16.PCI Adapter
- DIO Software
- User Manual

Installation

Card Setup

The **DIO-16.PCI** is a fully compliant PCI 'Plug and Play' adapter. All card resources (i.e. I/O address, IRQ selection) are auto-assigned by either your system BIOS or your 'Plug and Play' operating system.

Software Installation

For Windows Users

Choose **Install Software** at the beginning of the CD and select the **Digital I/O** software drivers and install **SeaIO**.

System Installation

The **DIO-16.PCI** can be installed in any of the PCI expansion slots.

- 1. Turn off PC power. Disconnect the power cord.
- 2. Remove the PC case cover.
- 3. Locate an available PCI slot and remove the blank metal slot cover.
- 4. Gently insert the **DIO-16.PCI** into the slot. Make sure that the adapter is seated properly.
- 5. Replace the screw.
- 6. Replace the cover.
- 7. Connect the power cord. Installation is complete.

Technical Description

The **DIO-16.PCI** provides two parallel input/output (I/O) ports. The ports are organized as ports A, B, C, and D. Port A is an input port interfaced to optically-isolated inputs, while port C is the reed relay output port.

Features

- 8 SPST relays
- 1 eight bit optically isolated input port
- DB-37 Male connector
- Highly reliable 10 VA DIP reed relays utilized
- Multiple adapters can reside in same computer
- PCI 2.1 Bus compatible

Input Port

Port A is 8 bit input port connected to optically isolated input sensors. Each sensor can be used to interface a voltage input and then sense whether the voltage is on or off. Each sensor is isolated (with respect to a common ground) from every other sensor, and also isolated with respect to the host PC ground. This means that signals such as low-level AC line voltage, motor servo voltage, and control relay signals can be 'sensed', or read by the PC, without the risk of damage due to ground loops or ground faults.

Each sensor input pair has a current limiting resistor that is used to limit the input current to the opto-isolator. The opto-isolator has two 'back-to-back' diodes internally. This allows AC or DC signals to be sensed, regardless of polarity. When the applied voltage is high enough to cause the LED in the opto-isolator to turn-on, the output of the opto-isolator goes low (0 volts) and the signal is read as a low logic level (binary 0) by the PC. When the input signal is too low to turn on the opto-isolator, the output goes high and the port bit is read by the PC as a high logic level (binary 1).

The input impedance of each isolated input is approximately 560 ohms (factory default). The opto-isolator requires approximately 3 mA to turn on. The maximum input current is 60 mA. Two things to consider when selecting the input resistor:

- 1. The first is turn on voltage for the circuit to sense.
- 2. The maximum input voltage. Maximum input voltage must not provide too much power to the input resistor, and must also not overdrive the opto-isolator input current specification. The following formulas apply:

Turn on current: 3 mA Isolator diode drop: 1.1 V Resistor power Max: .25 W

Turn on Voltage = diode drop + (turn on current) x (resistance)

Or:

$$1.1 + (.003) \times R$$

Maximum voltage = square root of (.25 (resistor value))

The following table shows four common input resistors and the ranges associated with each.

Input Resistor	Min Turn-On	Max Input	Max Current
(Ohms)	(Volts)	(Volts)	(mA)
220	1.76	7	27
560*	2.8	12	20
1K	4.1	16	15
2.2K	7.7	24	10

^{*}Factory default

Increasing the input resistor accordingly can increase the maximum input voltage. Because socketed DIP resistor networks are utilized, they can easily be replaced with a different value. This can be done at the factory, if necessary. The input circuits are <u>not</u> intended for monitoring 120-volt AC circuits.

Port A Bit	P1
0	2,20
1	3,21
2	4,22
3	5,23
4	6,24
5	7,25
6	8,26
7	9,27

Output Port (Reed Relay)

Reed relays provide very high quality, long life, low current (10 Watt maximum), dry contact switch closures. Reed relays are not suited for high current applications, and can be destroyed by inductive load switching, where a spark occurs across the contacts internally. The relays are normally open, and close when energized. Writing a '1' to the proper port bit can individually energize each relay.

Relay Specifications

• Contact Power Ratings: 10 Watts Maximum

Contact Voltage Maximum: 100 Volts DC or AC Maximum

• Contact Current Maximum: .5 Amps DC or AC RMS

• Contact Resistance, Initial: .15 Ohms

Rated Life:

Low Load: 200 Million Closures Maximum Load: 100 Million Closures

• Contact Speed:

Operate: .5 m Sec Release: .5 m Sec Bounce: .5 m Sec

• Maximum Operating Speed: 600 Hertz

Output Port (Reed Relay) Pin Assignments (P3 DB-37 Male)

Port C Bit	Relay	P2 Pin
0	K1	10,28
1	K2	11,29
2	K3	12,30
3	K4	13,31
4	K5	14,32
5	K6	15,33
6	K7	16,34
7	K8	17,35

Power and Ground Pin Assignments (P3 DB-37 Male)

Ground	18,36,37
+ 5 Volts	19
+ 12 Volts	1

Software

The **DIO-16.PCI** ships with Sealevel Systems' SeaI/O suite of Windows 95/98/NT drivers. SeaI/O provides the user with a consistent and straightforward API, allowing the developer to concentrate on the details of the application as opposed to low level driver development. Popular development environments, including Visual C++, Visual Basic, and Delphi, are supported for application development. SeaI/O includes a utility for configuring the driver parameters under Windows 95/98 and Windows NT, further simplifying installation.

For DOS, QNX, Linux and other operating systems, please refer to the software included with your card.

Programming

Application Programmers Interface (API)

Most modern operating systems do not allow direct hardware access. The SeaIO driver and API have been included to provide control over the hardware in Windows and Linux environments.

The purpose of this section of the manual is to help the customer with the mapping of the API to the actual inputs and relays for the 8002 specifically. Complete documentation of the API can be found in its accompanying help file.

Interrupts

Interrupt sampling can be set up in the API. Port A bit zero is the interrupt source. Refer to the API help file for more detailed information.

Relative Addressing vs. Absolute Addressing

The SeaIO API makes a distinction between "absolute" and "relative" addressing modes. In absolute addressing mode, the Port argument to the API function acts as a simple byte offset from the base I/O address of the device. For instance, Port #0 refers to the I/O address base + 0; Port #1 refers to the I/O address base + 1.

Relative addressing mode, on the other hand, refers to input and output ports in a logical fashion. With a Port argument of 0 and an API function meant to output data, the first (0^{th}) output port on the device will be utilized. Likewise, with a Port argument of 0 and an API function designed to input data, the first (0^{th}) input port of the device will be utilized.

In all addressing modes, port numbers are zero-indexed; that is, the first port is port #0, the second port is #1, the third #2, and so on.

Tables: API Port/bit reference numbers for Absolute and Relative Addressing

R = ReadW = Write

R/W = Read or Write

Port	API Port # Absolute	API Port #	Port Type
	Address (function)	Relative Address	
		(function)	
A	0(R)	0(R)	Input Port (Opto Input)
В	1		Unused
C	2 (R/W)	0 (W)	Output Port (Reed Relays)
D			Unused

API Bit # Absolute	API Bit # Relative	Port Bit
Address (function)	Address (function)	
0(R)	0(R)	A0 - Input
1(R)	1(R)	A1 - Input
2(R)	2(R)	A2 - Input
3(R)	3(R)	A3 - Input
4(R)	4 (R)	A4 - Input
5(R)	5(R)	A5 - Input
6(R)	6(R)	A6 - Input
7(R)	7(R)	A7 - Input
8(R)	8(R)	B0 - Input
16 (R/W)	0 (W)	C0 - Output
17 (R/W)	1 (W)	C1 - Output
18 (R/W)	2 (W)	C2 - Output
19 (R/W)	3 (W)	C3 - Output
20 (R/W)	4 (W)	C4 - Output
21 (R/W)	5 (W)	C5 - Output
22 (R/W)	6 (W)	C6 - Output
23 (R/W)	7 (W)	C7 - Output

Direct Hardware Control

In systems where the users program has direct access to the hardware (DOS) the table below gives the mapping and functions which the 8002 provides. The address of each eight-bit port is calculated as shown in the table on the following page, the cards base address plus an offset.

Reading the Inputs (direct):

The inputs are active Low. If no voltage is applied across one of the differential inputs it returns a one on that bit. If an AC or DC voltage (of sufficient magnitude, covered above) is applied it returns a zero on that bit.

Reading the Outputs (relays) (direct):

The relay ports return the ones complement of the value that is currently being used to drive the relays. When using the API the value is returned not the complement of the value.

Writing the Outputs (relays) (direct):

The output ports are the only ports that can be written. The relays on a standard 8002 are normally open. To close a relay a one must be written to the appropriate bit.

Interrupts

Interrupts can be set up as shown on the following page. Port A bit zero is the interrupt source.

Register Description

Address	Mode	D7	D6	D5	D4	D3	D2	D1	D0
Base+0	RD	PAD7	PAD6	PAD5	PAD4	PAD3	PAD2	PAD1	PAD0
Base+1	RD	{0}	{0}	{0}	{0}	{0}	{0}	{0}	{0}
Base+2	RD/WR	PCD7	PCD6	PCD5	PCD4	PCD3	PCD2	PCD1	PCD0
Base+3	RD	{0}	{0}	{0}	{0}	{0}	{0}	{0}	{0}
Base+4	RD	{0}	{0}	{0}	{0}	{0}	{0}	{0}	{0}
Base+5	RD/WR	IRQEN	IRQST	{0}	{0}	{0}	{0}	IRC1	IRC0
Base+6	RD	{0}	{0}	{0}	{0}	{0}	{0}	{0}	{0}
Base+7	RD	{0}	{0}	{0}	{0}	{0}	{0}	{0}	{0}

Table: Hardware register map.

RD = readWR = write

RD/WR = read or write

Note: When selecting the Interrupt Mode, always disable interrupts prior to changing or setting states. This will help prevent inadvertent or un-expected interrupts from occurring. When using the high and low level interrupts, a change in state of the input must occur before the interrupt can be cleared. The device providing the input to Base +0, bit D0 must do this.

PAD0-7 = Port A (Base+0) Input port. PCD0-7 = Port C (Base+2) Output port. IRC0-1= Interrupt Mode select (Base+5)

IRC1	IRC0	Interrupt Type
0	0	Low Level
0	1	High Level
1	0	Falling Edge
1	1	Rising Edge

IRQEN = enable interrupts (Base+5)

0 = disabled

1 = enabled (disabled after reset or power up).

IRQST = interrupt status (Base+5)

1 = interrupt pending (reading the bit clears interrupt). Bit can not be written.

Specifications

Environmental Specifications

Specification	Operating	Storage
Temperature	0° to 50° C	-20° to 70° C
Range	(32° to 122° F)	(-4° to 158° F)
Humidity Range	10 to 90% R.H.	10 to 90% R.H.
	Non-Condensing	Non-Condensing

Power Consumption

Supply line	+5 VDC	
Rating	280mA	

Mean Time Between Failures (MTBF)

Greater than 150,000 hours. (Calculated)

Physical Dimensions

Board Length	5.00 inches	(12.700 cm.)
Board Height including Goldfingers	3.900 inches	(9.906 cm.)
Board Height excluding Goldfingers	3.575 inches	(9.080 cm.)
Board Weight	3.2 ounces	(90.71g)

Appendix A - Troubleshooting

Following these simple steps can eliminate most common problems without the need to call Technical Support.

- 1. Install software <u>first</u>. After installing the software then proceed to adding the hardware. This places the required installation files in the correct locations.
- Identify all I/O adapters currently installed in your system. This includes your on-board serial ports, controller cards, sound cards etc. The I/O addresses used by these adapters, as well as the IRQ (if any) should be identified.
- 3. Ensure that there is no conflict with currently installed adapters. No two adapters can occupy the same I/O address and may not be allowed to share IRQs.
- 4. Make sure the Sealevel Systems adapter is securely installed in a motherboard slot.

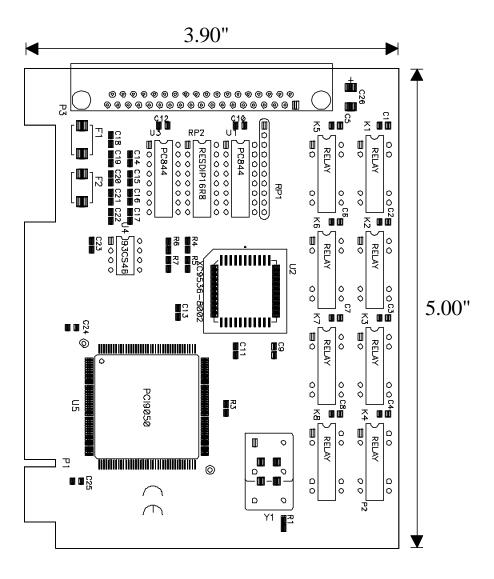
Appendix B - How To Get Assistance

Please refer to Troubleshooting Guide prior to calling Technical Support.

- 1. Read this manual thoroughly before attempting to install the adapter in your system.
- 2. When calling for technical assistance, please have your user manual and current adapter settings. If possible, please have the adapter installed in a computer ready to run diagnostics.
- 3. Sealevel Systems maintains a Home page on the Internet. Our home page address is www.sealevel.com. The latest software updates, and newest manuals are available via our FTP site that can be accessed from our home page.
- 4. Technical support is available Monday to Friday from 8:00 a.m. to 5:00 p.m. eastern time. Technical support can be reached at (864) 843-4343.

RETURN AUTHORIZATION MUST BE OBTAINED FROM SEALEVEL SYSTEMS BEFORE RETURNED MERCHANDISE WILL BE ACCEPTED. AUTHORIZATION CAN BE OBTAINED BY CALLING SEALEVEL SYSTEMS AND REQUESTING A RETURN MERCHANDISE AUTHORIZATION (RMA) NUMBER.

Appendix C - Silk-Screen



Appendix D - Compliance Notices

Federal Communications Commission Statement

FCC - This equipment has been tested and found to comply with the limits for Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference. In such case the user will be required to correct the interference at his own expense.

EMC Directive Statement



Products bearing the CE Label fulfill the requirements of the EMC directive (89/336/EEC) and of the low-voltage directive (73/23/EEC) issued by the European Commission.

To obey these directives, the following European standards must be met:

- EN55022 Class A "Limits and methods of measurement of radio interference characteristics of information technology equipment"
- EN55024-'Information technology equipment Immunity characteristics Limits and methods of measurement.
- EN60950 (IEC950) "Safety of information technology equipment, including electrical business equipment"

Warning

This is a Class A Product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

Always use cabling provided with this product if possible. If no cable is provided or if an alternate cable is required, use high quality shielded cabling to maintain compliance with FCC/EMC directives.

Warranty

Sealevel Systems, Inc. provides a lifetime warranty for this product. Should this product fail to be in good working order at any time during this period, Sealevel Systems will, at it's option, replace or repair it at no additional charge except as set forth in the following terms. This warranty does not apply to products damaged by misuse, modifications, accident or disaster.

Sealevel Systems assumes no liability for any damages, lost profits, lost savings or any other incidental or consequential damage resulting from the use, misuse of, or inability to use this product. Sealevel Systems will not be liable for any claim made by any other related party.

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Technical Support is available from 8 a.m. to 5 p.m. Eastern time.

Monday - Friday

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