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1394 SuperLINKCore™ Evaluation Board

Product Specification



Integrated Intellectual Property Inc.

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Features

- · Evaluation Board for 1394 applications
- Compliant with IEEE 1394-1995 specification
- Includes the I²P LINK Core on a Xilinx XC4062XL-09 FPGA
- · Includes an extra Xilinx FPGA for custom logic
- Adaptable to most Isochronous 1394 Applications
- Uses a 200 Mbps Texas Instruments PHY chip
- Two 1394 ports
- PCI 33 MHz interface through a V3 PCI Bridge chip
- PCMCIA interface

- · Parallel Port interface
- Onboard AMD microcontroller
- Slot for 256K dual-port RAM
- Optional software drivers

General Description

The SuperLINKCore[™] evaluation board is designed to provide the customer with maximum flexibility in designing and testing a 1394 design. The board provides PCI, PCMCIA and parallel port interfaces. The board uses a Texas Instruments physical layer chip to communicate with the LINK layer FPGA using the protocol in Annex J of the 1394-1995 specification.

An extra Xilinx FPGA is provided for users to create a complete 1394 solution with their own logic including the transaction layer. The FPGA can be configured to talk to a 256K dual-port RAM. Software drivers are optionally provided for specific applications.

Available Support Products

I²P supplies a complete line of hardware and software products for FireWire system development. Contact I²P for more information.

- · Software drivers.
- SuperLINKCore[™] FireWire Link Layer core.

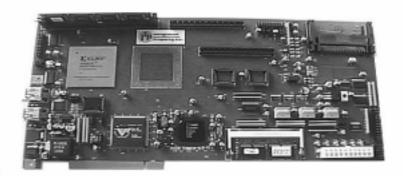


Figure 1: IEEE FireWire Evaluation Board

- A complete IEEE 1394 verification environment is available for testing LINK or PHY designs. It includes features for automatic test generation, automatic topology setup and a complete verification checklist.
- Technical support from I²P.

Ordering Information

The board and products referenced in this product description are available directly from Integrated Intellectual Property, Inc. Contact I²P for pricing or additional information.

Related Information

Xilinx Programmable Logic

For information on Xilinx programmable logic or development system software, contact your local Xilinx sales office, or: Xilinx, Inc.

2100 Logic Drive San Jose, CA 95124

Phone: +1 408-559-7778 Fax: +1 408-559-7114 URL: www.xilinx.com

For general Xilinx literature, contact:

Phone: +1 800-231-3386 (inside the US)

+1 408-879-5017 (outside the US)

E-mail: literature@xilinx.com

For AllianceCORE™ specific information, contact:

Phone: +1 408-879-5381 E-mail: alliancecore@xilinx.com

URL: www.xilinx.com/products/logicore/alliance/

tblpart.htm