

Hi-Rel Product Roadmap Provides High-Density Leadership

Even as defense spending continues to decrease, the funds devoted to electronics for “smart” weapons and defense systems are expected to grow. Designers in the Hi-Rel/Military applications market are spending wisely, focusing on development time and time-to-market in a highly competitive industry, and using the latest technologies.

Xilinx has created a high-density FPGA roadmap that, in conjunction with QML certification, makes Xilinx FPGAs the technology of choice for defense system designers.

As it becomes more and more difficult to obtain Hi-Rel ASICs, high-density FPGAs will become the standard for Hi-Rel logic design. This will be accomplished by focusing on selected products and offering both through-hole and surface mount packaging options.

Xilinx is already shipping the industry’s largest QML-compliant FPGA — the 25,000-gate XC4025E device.

With the trend towards lower supply voltages, the 3.3 volt XC4000XL family is destined to become the flagship of the Xilinx Hi-Rel product lines. The 62,000-gate XC4062XL FPGA will be available as a QML product in the first half of 1998.

Out strong product leadership will be further enhanced with the 2.5 volt, 125,000-gate XC40125XV device before the end of 1998, followed by a third generation family that will provide up to 400,000 gates of QML programmable logic in 1999.

Because FPGAs solve many of the problems facing the defense electronics industry, they have been successfully designed into hundreds of military and aerospace applications, such as electronic warfare, countermeasures, missile guidance, radar, sonar, communications and avionics systems. ♦

