

Xilinx is now shipping Foundation Series design solutions capable of supporting both VHDL and Verilog.

The Foundation Series Software Now Delivers VHDL and Verilog

Our Foundation Series software remains simple, yet continues to deliver complete front-to-back development tools supporting all Xilinx CPLD and FPGA devices. There are two schematic packages and two HDL packages. Both Foundation Series HDL packages include VHDL and Verilog, design entry, synthesis using the Synopsys FPGA Express, and implementation tools from Xilinx.

Increased Software Performance

The rapidly increasing performance capabilities of FPGA and CPLD technologies demand increased performance from design solutions. In fact, the latest architecture from Xilinx delivers 1,000,000 gates at speeds greater than 100MHz.

To meet that challenge, Foundation software solutions are continually enhanced to provide high-quality results in a ready-to-use package. The software delivers push-button tools without compromising device complexity, functionality, or density.

The Foundation HDL tools have been specifically designed to quickly move you through the HDL learning curve; the moment the tools are taken out of the box, you can quickly install the product and be implementing designs in minutes. If you have exceptional needs,

Xilinx offers comprehensive support services providing assistance on Xilinx technologies as well as advanced application assistance.

Powerful Synthesis

This past year, Xilinx added the Synopsys Express technology to the Foundation Series software. With Synopsys' history of providing state-of-the-art synthesis solutions for high-density designs, it's not surprising that the new Foundation Express product is particularly effective for large programmable logic devices. There are many advanced synthesis features in Foundation Express, including timing optimization, area optimization, graphical constraint entry and analysis.

New Product Configurations

There are four Foundation packages: Base, Standard, Base Express, and Express. Base and Standard packages support XABEL for CPLDs but do not support VHDL or Verilog. Base Express and Express support XABEL, VHDL, and Verilog languages. Although there are device size limits in the Base and Base Express products, all Spartan devices are supported by all Foundation packages. All of the Xilinx Foundation Series packages are available today for PC platforms running Windows 95 and Windows NT platforms.

The four Foundation Series packages



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