



# HardWire™ Array Prototype Approval Form

*This form certifies that the HardWire prototypes have been fully evaluated and function correctly in the intended application. Xilinx is authorized to commit this design to production upon receipt of customer Purchase Order. A change in the design will result in customer liability for any work in process.*

HardWire Array Device Number \_\_\_\_\_

Customer Internal Part Number \_\_\_\_\_

Name \_\_\_\_\_

Signature \_\_\_\_\_ Date \_\_\_\_\_

Title \_\_\_\_\_

Company \_\_\_\_\_

Address \_\_\_\_\_

City \_\_\_\_\_ State/Province \_\_\_\_\_ Country \_\_\_\_\_ Zip \_\_\_\_\_

Phone (     ) \_\_\_\_\_

Fax (     ) \_\_\_\_\_

E-mail \_\_\_\_\_

**For Xilinx Internal Use Only**

Xilinx Part Number (HPC Code) \_\_\_\_\_

Marketing \_\_\_\_\_ Engineering \_\_\_\_\_

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