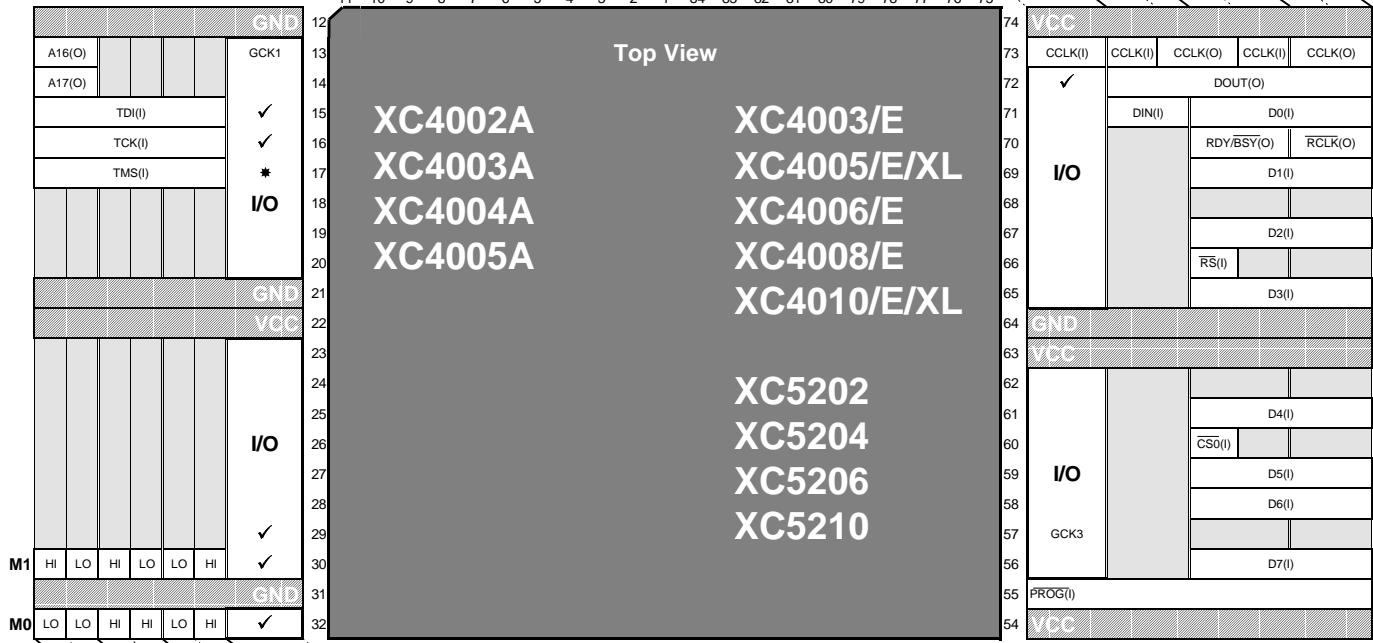


84-Pin PLCC XC4000/XC5200 Common Footprint

CONFIGURATION MODE

Configuration Mode										I/O										Clock Generation			
Master Parallel High										A15 (O) A14 (O) A13 (O) A12 (O) A11 (O) A10 (O) A9 (O) A8 (O)										A7 (O) A6 (O) A5 (O) A4 (O) A3 (O) A2 (O) A1 (O) A0 (O)			
Master Parallel Low																							
Sync. Periph. Mode																							
Async. Periph. Mode																				CS1(I) W(S)I			
Master Serial Mode																							
Serial Slave Mode																							
Normal Operation										✓ I/O										I/O GCK4 GND ✓			

11 10 9 8 7 6 5 4 3 2 1 84 83 82 81 80 79 78 77 76 75



M2

Pin Type	Number
General user I/O (no special symbols required)	59
JTAG as I/O (using TDO, TDI, TCK special symbols)	3
M2, M1, M0 as I/O (using M2, M1, M0 special symbols)	3
TOTAL POSSIBLE I/O PINS	65
Vcc pins	8
Ground (GND) pins	8
Dedicated control pins	3
TOTAL DEVICE PINS	84

	Pin	Special Pin Functions	As User I/O	
			XC4000	XC5200
✓	P10	XC4000 secondary buffer (SGCK1), not in XC5200	I/O	I/O
	P13	Global buffer input	I/O	I/O
✓	P15	Optional JTAG Test Data Input (TDI symbol)	I	I/O
✓	P16	Optional JTAG Test Clock input (TCK symbol)	I	I/O
*	P17	Optional JTAG Test Mode Select input (TMS)	I/O	I/O
✓	P29	XC4000 secondary buffer (SGCK2), not in XC5200	I/O	I/O
✓	P30	M1 mode pin (M1 symbol), RTRIG read trigger	O	I/O
✓	P32	M0 mode pin (M0 symbol), RDATA readback data	I	I/O
✓	P34	M2 mode pin (M2 symbol)	I	I/O
	P35	Global buffer input	I/O	I/O
✓	P51	XC4000 secondary buffer (SGCK3), not in XC5200	I/O	I/O
	P57	Global buffer input	I/O	I/O
✓	P72	XC4000 secondary buffer (SGCK4), not in XC5200	I/O	I/O
✓	P75	Optional JTAG Test Data Output (TDO symbol)	O	I/O
	P78	Global buffer input	I/O	I/O

✓ XC4000/XC5200 functional difference. Please check.

* Optional JTAG function.

Global clock buffer input.