

# Development System Solutions

**X**ilinx offers a variety of development system solutions, enabling the design and implementation of Xilinx Programmable Logic devices. These development systems combine the industry's fastest place and route technology, with a flexible and easy to use graphical interface to help you achieve the best possible designs within your project schedule, regardless of your experience level.

## Xilinx Alliance Series Software

<http://www.xilinx.com/products/alliance.htm>

The Alliance Series development systems are designed for customers who have made an investment in a customized EDA environment. The Xilinx Alliance Series software integrates into these environments by leveraging open systems standards, interfaces, and formats such as EDIF, SDF, VHDL, VITAL/Verilog, and STAMP. Combining the strengths of our EDA partners' tools with our advanced implementation features gives you the ultimate in flexibility and design performance.



## Xilinx Foundation Series Software

<http://www.xilinx.com/products/found.htm>

The Foundation Series development systems are designed for customer's who are interested in purchasing a complete, ready-to-use design solution for all of their programmable logic needs. Xilinx Foundation Series solutions enable both new and experienced programmable logic designers to achieve handcrafted results automatically, through push-button design flows. Foundation Series solutions support schematic-only, HDL-only, and mixed-level design flows by seamlessly integrating some of the industry's best EDA tools.



## ModelSim Xilinx Edition

<http://www.xilinx.com/products/software/mxe.htm>

The ModelSim Xilinx Edition (XE) simulator is a complete HDL simulation environment, optimized for use in verifying Xilinx programmable logic designs. ModelSim XE enables designers to verify the source code (VHDL and Verilog), as well as the functional and timing models of their design using a common "self-checking" testbench. ModelSim XE provides a



powerful first step into the world of HDL simulation with capacity and performance designed for the verification of the Xilinx XC9500 CPLD and Spartan series FPGA devices as well as lower-density XC4000 and Virtex series FPGAs.

ModelSim XE is most valuable for customers who understand the benefits of VHDL or Verilog simulation, and are looking for a cost-effective solution for low-density programmable logic design. It is available in both VHDL and Verilog versions. ModelSim XE may only be used with Xilinx v2.1i development systems and later. Xilinx sells the ModelSim XE products as options to any of its development systems.

## CPLD Web-powered Software Solutions

The Xilinx CPLD Web-powered Software Solutions offer designers the flexibility to do CPLD design evaluation and fitting on-line or on their desktop. The WebFITTER is an on-line device fitting and evaluation tool which accepts VHDL, Verilog, ABEL, or netlist files. The WebPACK downloadable desktop solutions offer free CPLD software modules from ABEL and HDL synthesis to device fitting and JTAG programming.

### WebFITTER

<http://www.xilinx.com/sxpresso/webfitter.htm>



The Xilinx WebFITTER is a free, Web-based CPLD design evaluation and fitting software tool that allows system designers to target their designs using the industry's best CPLDs, the XC9500 Series and the CoolRunner Series, on the latest version of Xilinx software and get their results and pricing in minutes.

### WebPACK

<http://www.xilinx.com/sxpresso/webpack.htm>



The Xilinx WebPACK contains free downloadable software solutions for Xilinx XC9500 and CoolRunner Series CPLDs. Each solution provides a simple and intuitive design environment for any Xilinx CPLD family. The WebPACK is a collection of three design suites: design entry, device fitting, and programming. These tools can be downloaded and used individually or, when installed together, become an integrated design environment for Xilinx CPLDs. 