


# Spartan-II Data Sheet

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The Spartan-II data sheet has been divided into the four modules described below.

Please update your bookmarks to the HTML version of this page ([ds001.htm](#)) or to the main Data Book page ([databook.htm](#)).

Module 1:  (90 KB) [Introduction and Ordering Information](#) v2.0 9/00

Feature summary, product table, overview, package & speed availability, maximum user I/O & package type, ordering code format.

Module 2:  (640 KB) [Functional Description](#) v2.0 9/00

Architecture details, software, configuration description & timing, design considerations.

Module 3:  (240 KB) [DC and Switching Characteristics](#) v2.0 9/00

Absolute max ratings, operating conditions, DC characteristics, switching characteristics (except configuration).

Module 4:  (250 KB) [Pinout Tables](#) v2.1 10/00

Pin definitions, pinout tables.

## Detailed Table of Contents

Module 1:  (90 KB) [Introduction and Ordering Information](#) v2.0 9/00

- Introduction
- Features
- General Overview
- Product Availability
- User I/O Chart
- Ordering Information

Module 2:  (640 KB) [Functional Description](#) v2.0 9/00

- Architectural Description
  - Spartan-II Array
  - Input/Output Block
  - Configurable Logic Block
  - Programmable Routing Matrix
  - Clock Distribution: Delay-Locked Loop
  - Boundary Scan
- Development System
- Configuration
  - Timing
  - Readback
- Design Considerations
  - Using Delay-Locked Loops
  - Using Block SelectRAM+ Features
  - Using Versatile I/O

Module 3:  (240 KB) [DC and Switching Characteristics](#) v2.0 9/00

- DC Specifications
  - Absolute Maximum Ratings
  - Recommended Operating Conditions
  - DC Characteristics
- Switching Characteristics
  - Pin-to-Pin Parameters
  - IOB Switching Characteristics
  - Clock Distribution Characteristics
  - DLL Timing Parameters
  - CLB Switching Characteristics
  - Block RAM Switching Characteristics
  - TBUF Switching Characteristics
  - JTAG Switching Characteristics

Module 4:  (280 KB) [Pinout Tables](#) v2.1 10/00

- Pin Definitions
- Pinout Tables