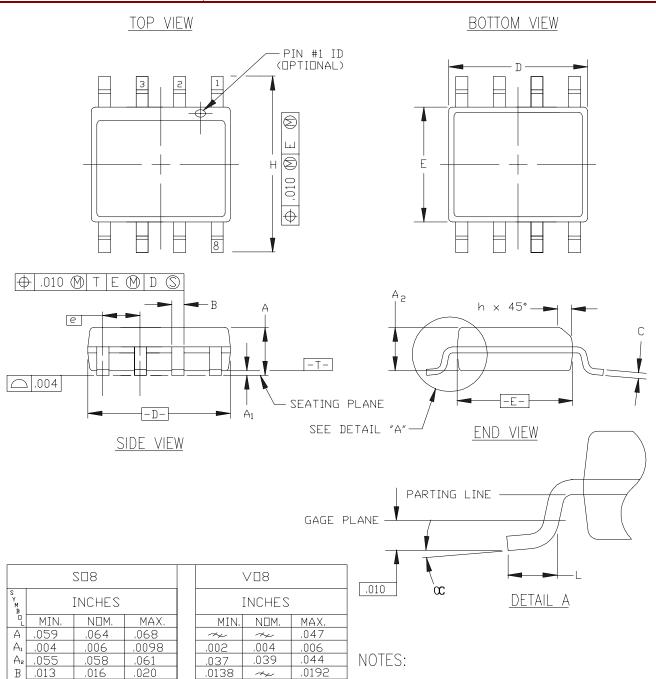
SOIC (SO8) and TSOP (VO8) Packages

PK002 (v1.0) June 1, 2000



NOTES:

- 1. ALL DIMENSIONS AND TOLERANCES CONFORM TO ANSI Y14.5M-1982.
- 2. DIMENSION 'D' DOES NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION SHALL NOT EXCEED .006" PER SIDE.
- 3. DIMENSION 'E' DOES NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION SHALL NOT EXCEED .010 INCH PER SIDE.
- 4. LEAD FINISH: SOLDER PLATE

8 LEAD SOIC/TSOP (SO8, VO8)

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