



- NOTES: 1. P1 is a 96 pin male connector, 3 rows (A,B,C) of 32 pins.
 2. S1 is a 280 pin CHIP SCALE Socket.
 3. M1 are connection pads for an autohandler.
 4. Board ID is B4 hex, binary 1011 0100
 5. Signal names for XCR3256XL
 5. Signal names for P1-B8 and P1-B31 are for HW-130 ML7 only and were formerly PVSP.
 6. FPGA Design is modified to accept TMS on A1 or A2.

		TITLE			
		SCHEM, HW-136-CS280 COOLRUNNER XPLA3 ADAPTER (ASSY # 0431103)			
DRAWN BY Carol		DATE 5/9/00		PROJECT: HW-130 ADAPTERS	
CHECKED BY		DATE		SIZE	DRAWING NUMBER
ENG APPROVAL		DATE		B	0381083
MFG APPROVAL		DATE			
<p>TOLERANCES</p> <p>∠ .x .xx .xxx</p> <p>Unless otherwise specified, dimensions are in inches.</p>				SCALE NONE	SHEET 1 OF 1