New LogiCORE PCI Target Eases System Integration

The new LogiCORE™ PCI Slave (target-only) v1.1 has been shipped to all LogiCore PCI owners with current maintenance agreements. This updated release will further simplify integration of the core with the user's unique back-end logic. The design has been fine-tuned and constraints (e.g., new guide files) have been added. As a result, timing has improved, and the new version is more robust than its predecessor.

The improved User's Guide has been restructured to take the designer step-by-step through the design process. In addition, two new documents have been added to the product: a LogiCORE PCI Check List detailing the PCI commands that are supported by the core, and a PCI reference text book, *PCI Systems Architecture* (T. Shanley and D. Anderson, Mindshare Press). Furthermore, support for several new devices and package options has been added *(see table).*
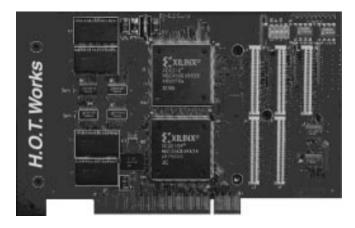
For complete data sheets and other information, please visit the LogiCORE site at WebLINX (www.xilinx.com/products/logicore/logicore.htm). ◆

DEVICE	PACKAGE	PCI MASTER	PCI SLAVE
XC4020E	HQ240	April 1997	✓*
	PQ208	April 1997	✓*
XC4013E	HQ240	April 1997	✓
	PQ208	✓	✓
	PQ160		✓

* Available on the web for registered LogiCORE customers

Virtual Computing Debuts XC6200 Development Kit

Virtual Computing Corp. (Reseda, CA) has introduced the H.O.T. Works 6200 Development System, a complete programming and development system for the Xilinx XC6200 family of Reconfigurable Processing Units (RPUs). H.O.T. Works 6200 includes the PCI-XC6200 plug-in board and a number of software development tools:

➤ **The Lola Programming System** — a simple, object-oriented hardware description language for describing synchronous, digital circuits that was developed at ETH Zurich by Niklaus Wirth (the author of Pascal and Modula2)

➤ **XACT-6000™** — the Xilinx implementation tool set for the XC6200 family

➤ **XC6200 VHDL Elaborator** — a VHDL analyzer and EDIF netlist generator

➤ **Hardware Object Technology (H.O.T.) Interface** — tools that provide for the insertion of designs into executable C programs, enabling run-time reconfigurable computing

➤ **WebScope 6200** — a Java tool for real-time design emulation using the PCI-XC6200 board

➤ Design and application examples

The PCI-XC6200 board is a single PCI bus board featuring an XC6216 RPU, XC4013E FPGA (used for the PCI interface), 512Kb of SRAM, a programmable oscillator, and PCI mezzanine connectors for daughter boards.

List price for the entire kit is $995. For further information, contact Virtual Computer Corp. (tel: 818-342-8294; fax: 818-342-0240; e-mail: info@vcc.com) or visit its web page at www.vcc.com. ◆